



# Product Change Notification

**Change Notification #: 116759 - 00**

**Change Title:** Select Boxed Intel® Xeon® E-2124 Processor  
Select Boxed Intel® Xeon® E-2124G Processor,  
PCN 116759-00, Product Design,  
Removing "Intel® Hyper-Threading  
Technology" text from product packaging

**Date of Publication:** February 08, 2019

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

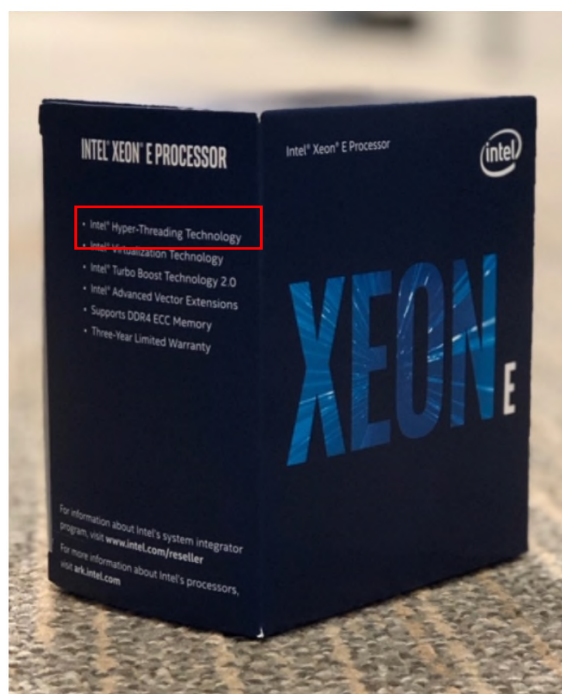
<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	March 11, 2019
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## Description of Change to the Customer:

The products listed in the "Products Affected / Intel Ordering Codes" table below will undergo the following change:

- The boxes of the Intel® Xeon® E-2124G and E-2124 processors incorrectly state that "Intel® Hyper-Threading Technology" is included. Intel is updating the box artwork to remove the "Intel® Hyper-Threading Technology" text. Product Packaging produced after March 11, 2019 will contain the corrected text.

Current label artwork being removed (highlighted in red):



## Customer Impact of Change and Recommended Action:

This change is to the product boxing (carton) itself and not the product (processor and thermal solution). This does not impact the Intel® Xeon® E-2124G and E-2124 processors capabilities.

Intel will immediately stop shipping these products with the current artwork. Intel is asking customers who have already purchased these products to return **ALL** impacted units by following the Return Material Authorization (RMA) process for product returns and replacements. Customers should return **ALL** impacted units by **March 4, 2019**.

For further questions and concerns, please contact your Intel representative or reach out to:  
<https://www.intel.com/support>

## Products Affected / Intel Ordering Codes:

Marketing Name	Processor#	Frequency	Product Code	S-Spec	MM#
Boxed Intel® Xeon® E-2124 Processor	E-2124	3.3 GHZ	BX80684E2124	S R3WQ	973772
Boxed Intel® Xeon® E-2124G Processor	E-2124G	3.4 GHZ	BX80684E2124G	S R3WL	977233

## PCN Revision History:

**Date of Revision:**

February 8, 2019

**Revision Number:**

00

**Reason:**

Originally Published PCN



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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific/PRC Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

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