

PRIMERGY BX920 S3

System configurator and order-information guide

January 2014

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PRIMERGY Server



Instructions

This document contains basic product and configuration information that will enable you to configure your system via System-Architect.

Only the tool "System-Arcitect" will ensure a fast and proper configuration of your PRIMERGY server or your complete PRIMERGY Rack system.

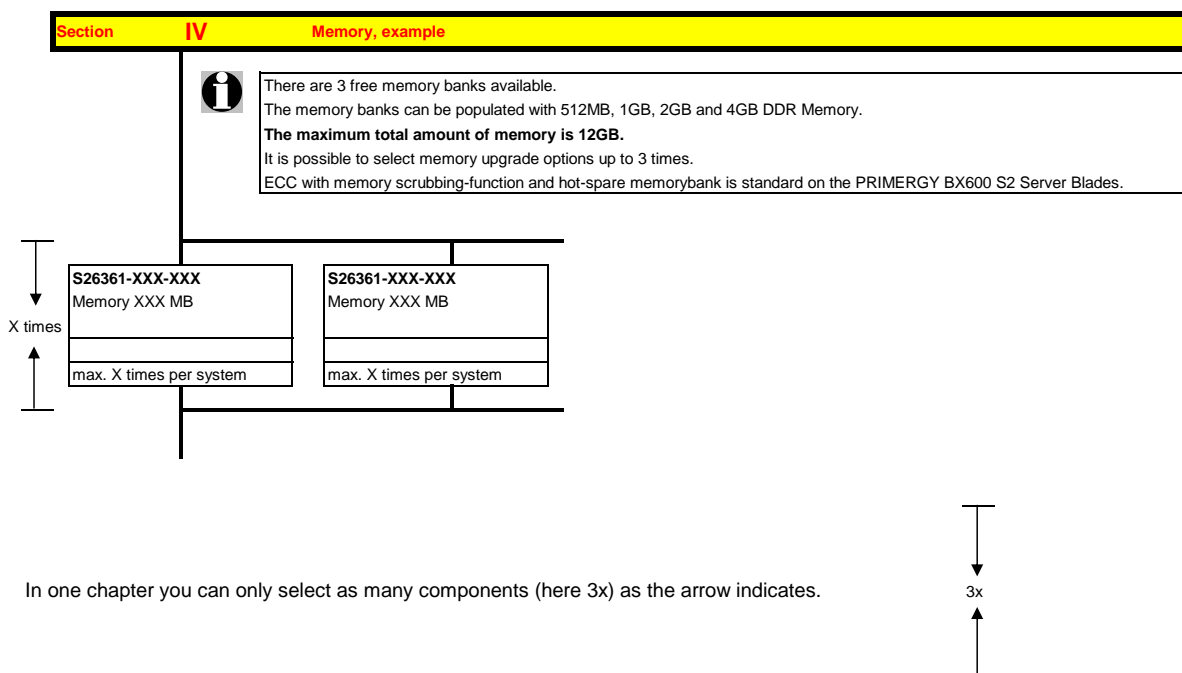
Please pay attention to the naming conventions:

BX900 S1	System unit 1nd generation
BX920 S2	Dual Server Blade S2

You can configure your individual PRIMERGY server in order to adjust your specific requirements.

The System configurator is divided into several chapters that are identical to the current price list and PC-/ System-Architect.

Please follow the lines. If there is a junction, you can choose which way or component you would like to take. Go through the configurator by following the lines from the top to the bottom.



In one chapter you can only select as many components (here 3x) as the arrow indicates.

Please note that there are information symbols which indicate necessary information.



For further information see:

http://ts.fujitsu.com/products/standard_servers/index.html

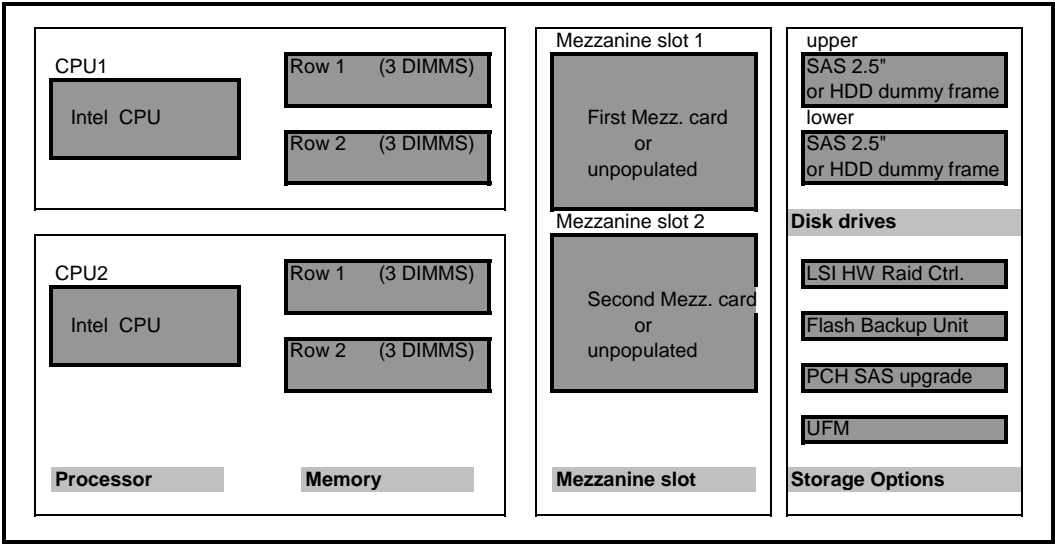
(internet)

https://partners.ts.fujitsu.com/com/order-supply/configurators/primergy_config/Pages/Currentconfigurators.aspx

(extranet)

Prices and availability see price list and PC-/ System-Architect
Subject to change and errors excepted

Configuration diagram Dual Server Blade BX920 S3

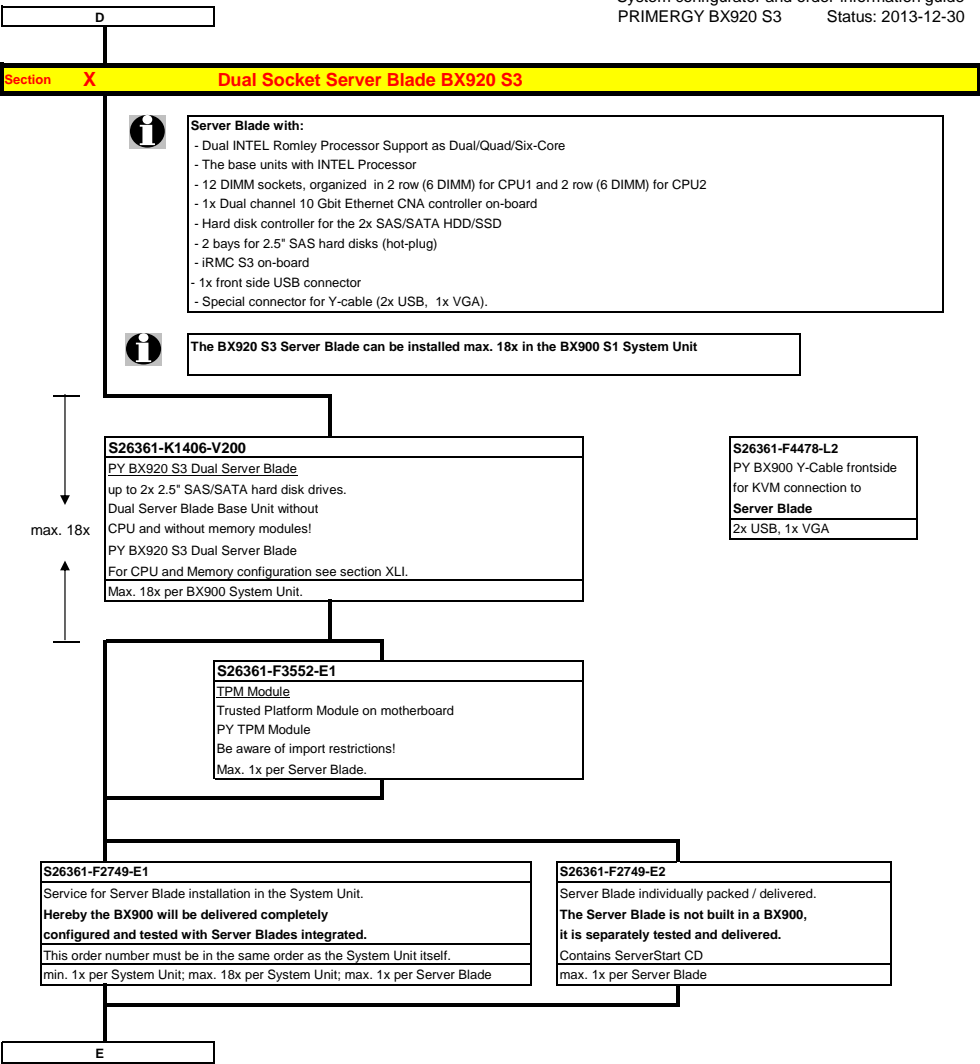


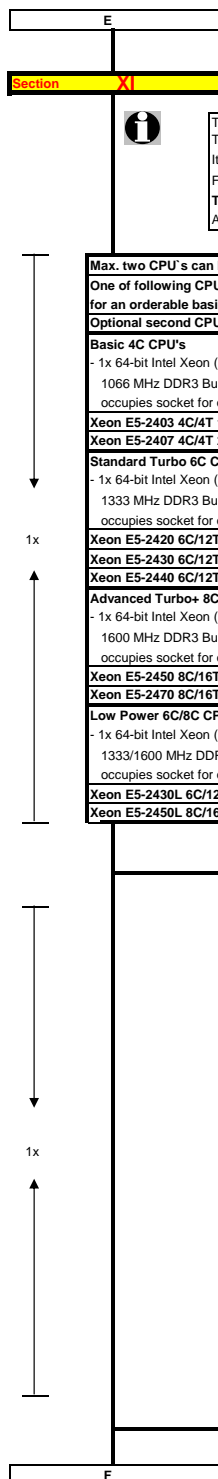
Key:

Included in basic unit Option

The population order for the CPU is: CPU1 first, then CPU2

The population order for the DIMMs: for each CPU, the DIMM row 1 (DIMMS 1A 1B 1C) (DIMMS 1D 1E 1F) first, then row 2 (DIMMS 2A, 2B, 2C) (DIMMS 2D, 2E, 2F)





There are 2 processor sockets available.
The first socket is always equipped with the **first CPU** which can be selected via configurator
It is also possible to upgrade a dual-processor system later on with a **second CPU**
For the second CPU there are different order numbers, due to the different Heatsink.
Two processors with different clock frequencies are not possible
A multi-processor operating system is required for a dual-processor system.

Max. two CPU's can be selected per basic unit		
One of following CPU's has to be selected as first CPU for an orderable basic unit		
Optional second CPU has to be the same type like the first CPU		
Basic 4C CPU's		
- 1x 64-bit Intel Xeon (10MB shared TLC = Third Level Cache) 1066 MHz DDR3 Bus, 6,40 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2403 4C/4T 1.80GHz 10MB 6.40GT/s 1066MHz 80W		S26361-F4560-E180
Xeon E5-2407 4C/4T 2.20GHz 10MB 6.40GT/s 1066MHz 80W		S26361-F4560-E220
Standard Turbo 6C CPU's		
- 1x 64-bit Intel Xeon (15MB shared TLC = Third Level Cache); Hyper-Threading (HT); 1333 MHz DDR3 Bus, 7,20 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2420 6C/12T 1.90GHz 15MB 7.20GT/s 1333MHz 95W		S26361-F4561-E190
Xeon E5-2430 6C/12T 2.20GHz 15MB 7.20GT/s 1333MHz 95W		S26361-F4561-E220
Xeon E5-2440 6C/12T 2.40GHz 15MB 7.20GT/s 1333MHz 95W		S26361-F4561-E240
Advanced Turbo+ 8C CPU's		
- 1x 64-bit Intel Xeon (20MB shared TLC = Third Level Cache); Hyper-Threading (HT); 1600 MHz DDR3 Bus, 8,00 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2450 8C/16T 2.10GHz 20MB 8.00GT/s 1600MHz 95W		S26361-F4562-E210
Xeon E5-2470 8C/16T 2.30GHz 20MB 8.00GT/s 1600MHz 95W		S26361-F4562-E230
Low Power 6C/8C CPU's		
- 1x 64-bit Intel Xeon (15/20MB shared TLC = Third Level Cache); Hyper-Threading (HT); 1333/1600 MHz DDR3 Bus, 8,00 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2430L 6C/12T 2.00GHz 15MB 7.20GT/s 1333MHz 60W		S26361-F4563-E200
Xeon E5-2450L 8C/16T 1.80GHz 20MB 8.00GT/s 1600MHz 70W		S26361-F4563-E180

Max. two CPU's can be selected per basic unit		
One of following CPU's has to be selected as second CPU for an orderable basic unit		
Optional second CPU has to be the same type like the first CPU		
Basic 4C CPU's		
- 1x 64-bit Intel Xeon (10MB shared TLC = Third Level Cache) 1066 MHz DDR3 Bus, 6,40 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2403 4C/4T 1.80GHz 10MB 6.40GT/s 1066MHz 80W		S26361-F4564-E180
Xeon E5-2407 4C/4T 2.20GHz 10MB 6.40GT/s 1066MHz 80W		S26361-F4564-E220
Standard Turbo 6C CPU's		
- 1x 64-bit Intel Xeon (15MB shared TLC = Third Level Cache); Hyper-Threading (HT); 1333 MHz DDR3 Bus, 7,20 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2420 6C/12T 1.90GHz 15MB 7.20GT/s 1333MHz 95W		S26361-F4565-E190
Xeon E5-2430 6C/12T 2.20GHz 15MB 7.20GT/s 1333MHz 95W		S26361-F4565-E220
Xeon E5-2440 6C/12T 2.40GHz 15MB 7.20GT/s 1333MHz 95W		S26361-F4565-E240
Advanced Turbo+ 8C CPU's		
- 1x 64-bit Intel Xeon (20MB shared TLC = Third Level Cache); Hyper-Threading (HT); 1600 MHz DDR3 Bus, 8,00 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
Xeon E5-2450 8C/16T 2.10GHz 20MB 8.00GT/s 1600MHz 95W		S26361-F4566-E210
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Low Power 4C/6C/8C CPU's		
- 1x 64-bit Intel Xeon (15/20MB shared TLC = Third Level Cache); Hyper-Threading (HT); 1333/1600 MHz DDR3 Bus, 8,00 GT/s QPI Bus and passive heat sink occupies socket for one CPU		
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Section XII Storage



Mixed configurations with ECO SATA drives and SAS drives are not allowed
Mixed configurations BC-SATA/BC-SAS drives with SAS 10K (S26361-F4482-E130, S26361-F4482-E145, S26361-F4482-E160) not allowed.
Configurations with Eco SATA can only be mixed with BC SATA HDD type
All combinations of SSD, BC SATA and SAS are possible - but not in same logical drive (RAID array)

Both hard disks are plugged in directly connected to the onboard controller.
One UFM can be configured in addition to the Hard Disks
Remark: UFM is part of the VMWare Embedded solution (S26361-F2341-E431)
Remark: Currently VMWare RAID configurations are not supported with the onboard PCH controller



SAS Drives require the SAS Option to enable PCH SAS connection or the SAS RAID Option

S26361-F5225-E100 SSD SATA 100GB, MLC Solid State Disk SATA 6Gb/s Mainstream Performance hot plug/hot replace tray max. 2x per base unit	S26361-F5225-E200 SSD SATA 200GB, MLC Solid State Disk SATA 6Gb/s Mainstream Performance hot plug/hot replace tray max. 2x per base unit	S26361-F5225-E400 SSD SATA 400GB, MLC Solid State Disk SATA 6Gb/s Mainstream Performance hot plug/hot replace tray max. 2x per base unit
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S26361-F4581-E100 SSD SAS 100GB, MLC Solid State Disk SATA 6Gb/s Mainstream Performance hot plug/hot replace tray max. 2x per base unit	S26361-F4581-E200 SSD SAS 200GB, MLC Solid State Disk SAS 6Gb/s Enterprise Performance hot plug/hot replace tray max. 2x per base unit	S26361-F4581-E400 SSD SAS 400GB, MLC Solid State Disk SATA 6Gb/s Mainstream Performance hot plug/hot replace tray max. 2x per base unit
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max 2x

S26361-F3708-E250 HD 250GB 7.2krpm 2.5" 7200rpm, <9.5ms, 64MB Cache BC SATA 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F3708-E500 HD 500GB 7.2krpm 2.5" 7200rpm, <9.5ms, 64MB Cache BC SATA 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F3708-E100 HD 1000GB 7.2krpm 2.5" 7200rpm, <9.5ms, 64MB Cache BC SATA 6Gb/s hot plug/hot replace tray max. 2x per base unit
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S26361-F4482-E573 HD 73GB 15krpm 2.5" 15000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F4482-E514 HD 146GB 15krpm 2.5" 15000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F4482-E530 HD 300GB 15krpm 2.5" 15000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit
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S26361-F5247-E130 HD 300GB 10krpm 2.5" 10000rpm, <4.5ms, 64MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F5247-E145 HD 450GB 10krpm 2.5" 10000rpm, <4.5ms, 64MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F5247-E160 HD 600GB 10krpm 2.5" 10000rpm, <4.5ms, 64MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit
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S26361-F4482-E190 HD 900GB 10krpm 2.5" 10000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F5247-E190 HD 900GB 10krpm 2.5" 10000rpm, <4.5ms, 64MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F5228-E500 HD 500GB 7.2krpm 2.5" 7200rpm, <4.5ms, 64MB Cache BC SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit
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S26361-F5228-E100 HD 1TB 7.2krpm 2.5" 7200rpm, <4.5ms, 64MB Cache BC SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit

S26361-F4482-E130 HD 300GB 10krpm 2.5" 10000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F4482-E145 HD 450GB 10krpm 2.5" 10000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit	S26361-F4482-E160 HD 600GB 10krpm 2.5" 10000rpm, <4.5ms, 8MB Cache SAS 6Gb/s hot plug/hot replace tray max. 2x per base unit
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Section XIII RAID Functionality on Server Blade



The Dual Server Blade supports different RAID solutions for internal HDD's

1. PCH based RAID
 2. PCH based RAID with SAS upgrade
 3. SAS RAID HDD Module with LSI HW RAID w/o cache
 4. SAS RAID HDD Module with LSI HW RAID w/ 512 MB Cache and optional FBU
- Also the SAS BP has to be changed. It is included in the RAID Mezz HDD Connection Kit.



Configuration Hint - Second CPU needed for SAS RAID Modules or SX910/940/960

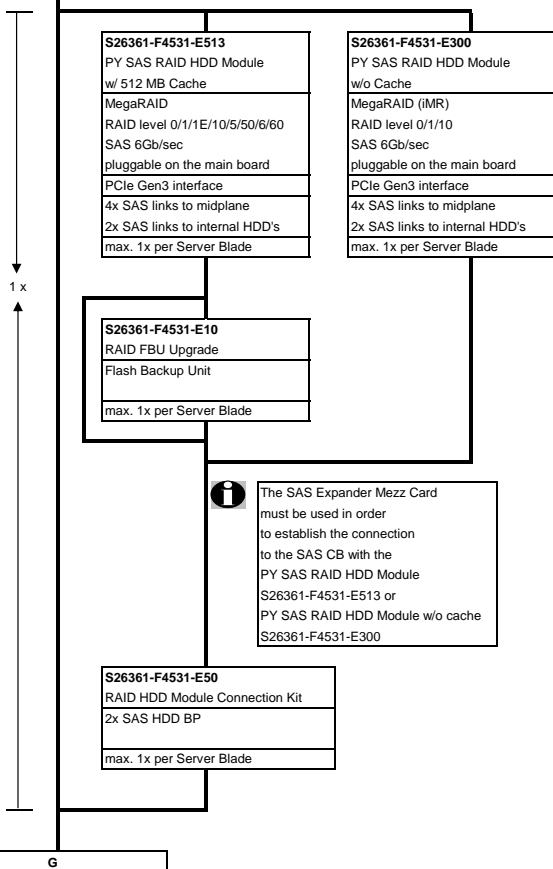
The SAS RAID Modules are only supported if second CPU is installed
The connection to the SX910/SX940/SX960 is only supported if second CPU is installed

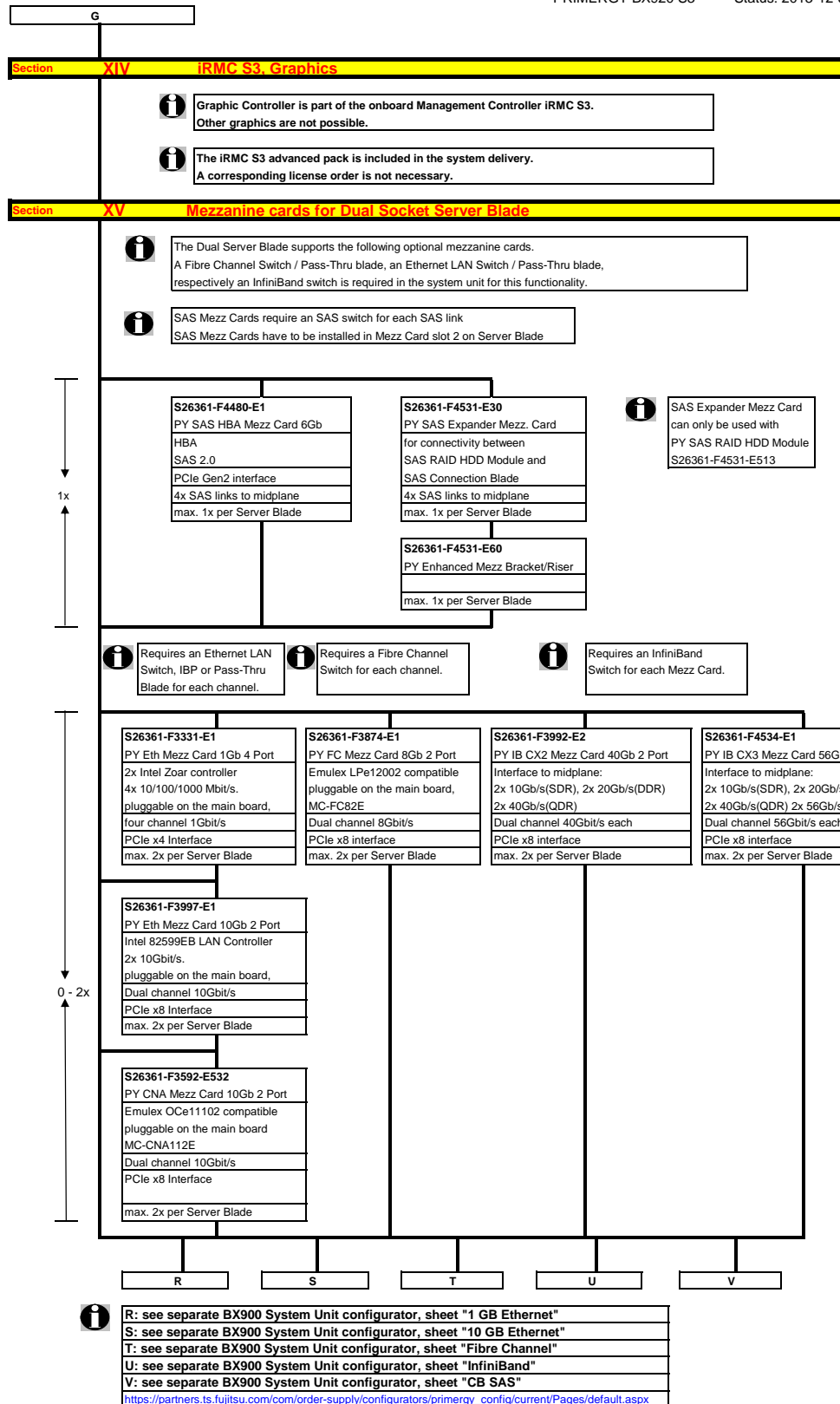
S26361-F3674-E1

Onboard Controller SAS upgrade
LSI
Patsburg B
RAID 0, 1 & 10
no controller cache
SAS 3Gb/sec
4 internal ports
PCIe x4
no PCI slot required
max. 1x per Server Blade



This upgrade is required to add SAS support to the PCH controller





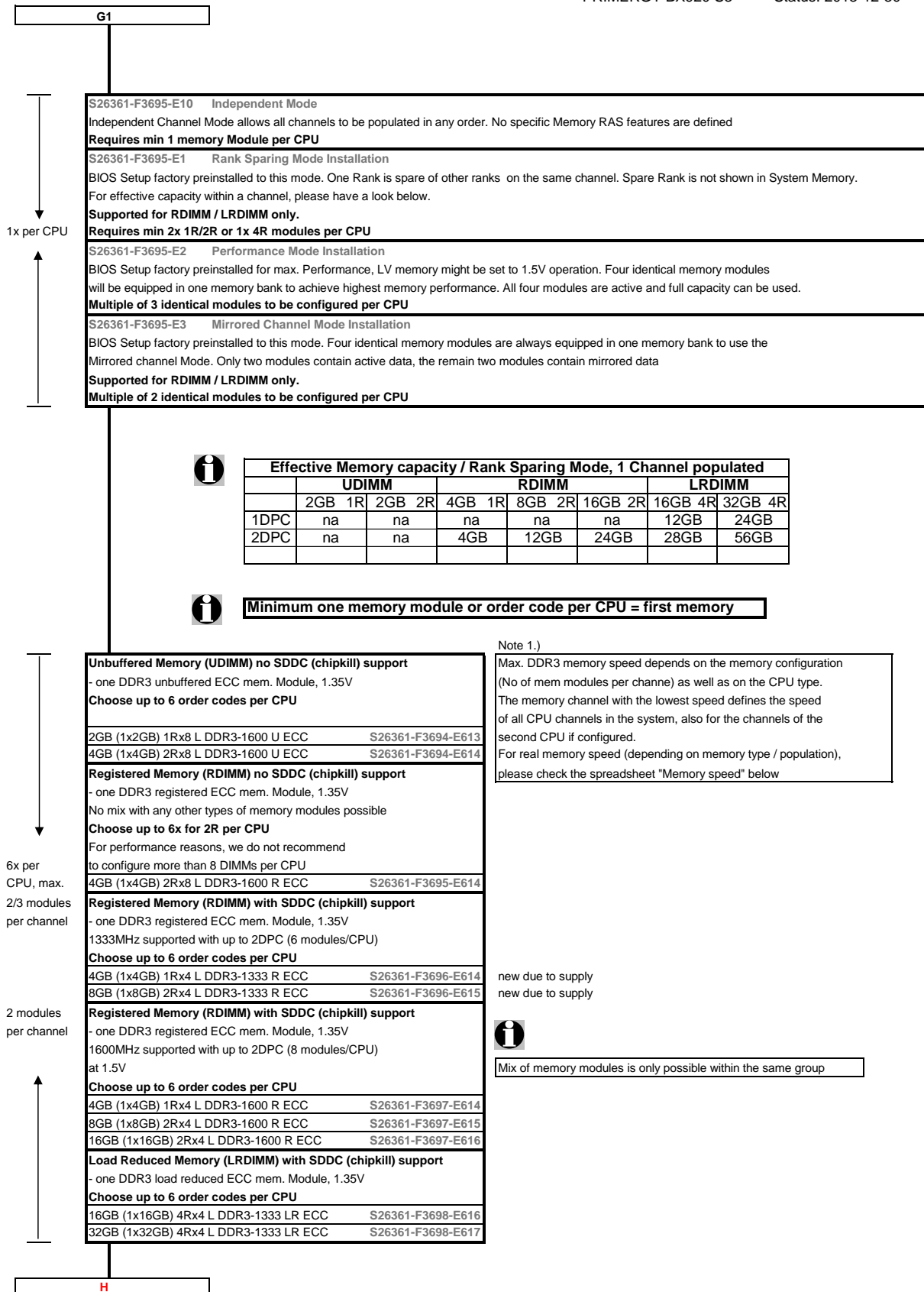
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Section III Memory



<p>- There are 6 memory slots per CPU for max. 192GB LRDIMM (6x 32GB 4R) 96GB RDIMM (6x 16GB 2R) 24GB UDIMM (6x 4GB) => max. 384GB for two CPU's (192GB per CPU), using LRDIMM</p> <p>- The memory area is divided into 3 channels per CPU with 2 slots per channel</p> <p>- Slot 1 of each channel belongs to memory bank 1, the slot 2 belongs to memory bank 2, slot 3 belongs to memory bank 3</p>
<p>Registered, LR DIMMs and unbuffered memory modules can be selected No mix of registered, load reduced and unbuffered modules allowed. Memory can be operated at 1.5V or 1.35V, even if the modules are of low voltage type. Memory operating voltage can be set within BIOS (1.5V is default setting for max. speed). In a 2 DIMMs per channel configuration, following frequencies are supported: - 1.5V - 1600MHz max (depending on CPU, special memory modules) - 1.35V - 1333MHz max (depending on CPU) SDDC (Chipkill) is supported for registered / load reduced x4 organized memory modules only</p>
<p>1.) In the "Independent Channel Mode" is following configuration possible Channels can be populated in any order in Independent Channel Mode. All four channels may be populated in any order and have no matching requirements. All channels must run at the same interface frequency but individual channels may run at different DIMM timings (RAS latency, CAS latency, and so forth) No mix of registered, load reduced and unbuffered modules allowed.</p>
<p>2.) "Rank Sparing Mode" configuration - Within a memory channel, one rank is a spare of the other ranks. The Spare Rank is held in reserve and is not available as system memory For the effective memory capacity, please refer to the spreadsheet below. The BIOS is set to the rank sparing setting. Minimum configuration is: 2x 1R, 2x 2R or 1x4R DDR3 module per channel This mode is not supported by x8 organized memory modules</p>
<p>3.) "Performance Mode" configuration - In this configuration, the memory module population ex factory is spread across all channels. The BIOS is set to the max. performance for memory. Minimum configuration is: 3x identical modules</p>
<p>4.) In the "Mirrored Channel Mode" is following configuration possible - Each memory bank can optionally be equipped with 4x registered or load reduced In each memory bank channel A and B / C and D of CPU 1 or channel E and F / G and H of CPU 2 have to be equipped with identical modules for mirrored channel mode. In channel B is always the mirrored memory of channel A of CPU 1 In channel E is always the mirrored memory of channel D of CPU 2 Minimum configuration is: 2x identical modules This mode is not supported by x8 organized memory modules</p>

G1



Memory Configuration PRIMERGY BX920 S3

Each CPU offers 6 **Slots** for DDR3 Memory Modules organised in **2 Banks and 3 Channels**.

If you need more than 6 Slots you have to configure the 2nd CPU.

Depending on the amount of memory configured you can decide between 4 basic modes of operation (see explanation below).

There are 3 different kinds of DDR3 Memory Modules available: UDIMM / RDIMM and LRDIMM

UDIMM / RDIMM / LRDIMM offer different functionality. Mix of UDIMM / RDIMM / LRDIMM is not allowed.

If 1.5V and 1.35V DIMMs are mixed, the DIMMs will run at 1.5V

Mode	Configuration	UDIMM	RDIMM	RDIMM LRDIMM	Application
		x8	x8	x4	
SDDC (chipkill) support	any	no	no	yes	detect multi-bit errors
Independent Channel Mode	1, 2 or 3 Modules per Bank	yes	yes	yes	offers max. flexibility, upgradeability, capacity use UDIMM modules for lowest cost
Mirrored Channel Mode *)	2 identical Modules / Bank	no	no	yes	offers maximum security
Performance Mode	3 identical Modules / Bank	yes	yes	yes	offers maximum performance and capacity
Rank Sparing Mode *)	min. 2 Ranks / Channel	no	no	yes	balances security and capacity

*) For the delivery ex works the system will be prepared with dedicated BIOS setting.

Capacity	Configuration	UDIMM	RDIMM	LRDIMM	Notes
Min. Memory per CPU	1 Module / CPU	1x2GB	1x4GB	1x 16GB	with one CPU
Max. Memory per CPU	4/6 Modules / CPU	6x4GB	6x16GB	6x 32GB	with one CPU
Max. Memory per System	8/12 Modules / System	48GB	96GB	384GB	if second CPU is configured

Memory-Speed:

Max. DDR3 memory speed depends on the memory configuration on one memory channel and the speed of the CPU

The memory channel with the lowest speed defines the speed of all CPU channels in the system

Mem. Speed provided by CPU	Real maximum memory-bus speed depending on CPU type, memory configuration (DPC) and voltage setting (BIOS)																	
	UDIMM 1600MHz						RDIMM 1600MHz						LRDIMM 1333MHz					
	1.5V [default]			1.35V			1.5V [default]			1.35V			1.5V [default]			1.35V		
	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3	1	2	3
	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC	DPC
CPU with 1600MHz DDR3 Bus	1333	1333	-	1066	1066	-	1600	1600	-	1333	1333	-	1333	1333	-	1066	1066	-
CPU with 1333MHz DDR3 Bus	1333	1333	-	1066	1066	-	1333	1333	-	1333	1333	-	1333	1333	-	1066	1066	-
CPU with 1066MHz DDR3 Bus	1066	1066	-	1066	1066	-	1066	1066	-	1066	1066	-	1066	1066	-	1066	1066	-

1R - Single Rank
2R - Dual Rank
4R - Quad Rank

on special release
as soon as available

1DPC = 1 DIMM per Channel
2DPC = 2 DIMM per Channel
3DPC = 3 DIMM per Channel

Configuration hints:

- The memory sockets on the systemboard offer a color coding:

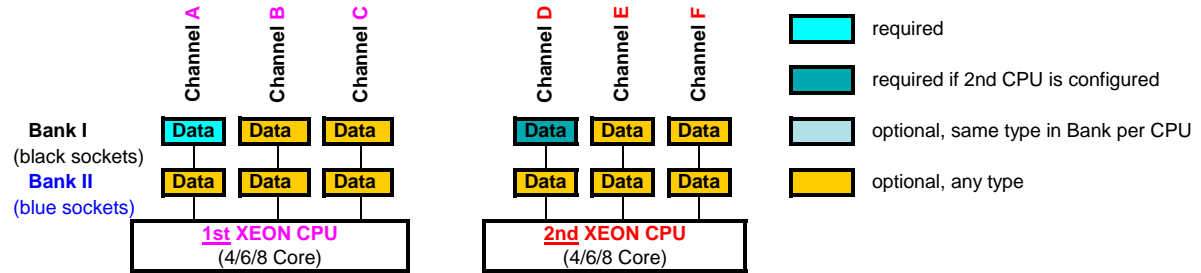
Bank I black sockets
Bank II blue sockets
Bank III green sockets

- A so called Bank consists of 1 memory module on every Channel available on one CPU (examples see below)

Bank I on CPU 1/2 up to 3 memory modules connected to Channel A - F on the 1st/2nd CPU
Bank II on CPU 1/2 up to 3 memory modules connected to Channel A - F on the 1st/2nd CPU

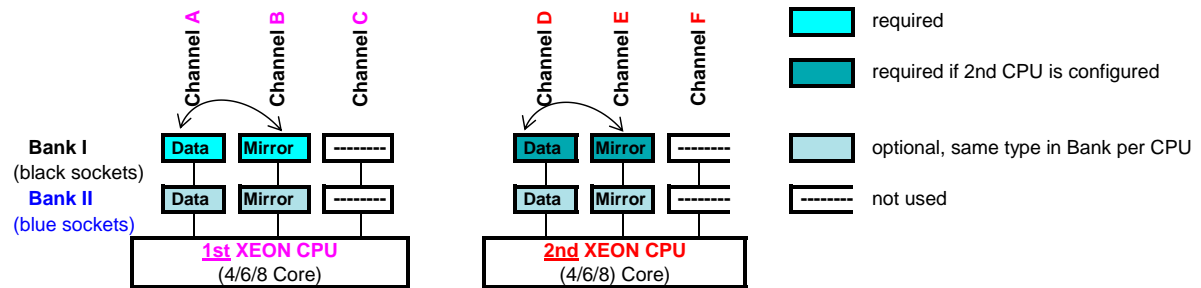
- See below and next page for a detailed descriptions of the memory configuration supported.

1. Independent Channel Mode



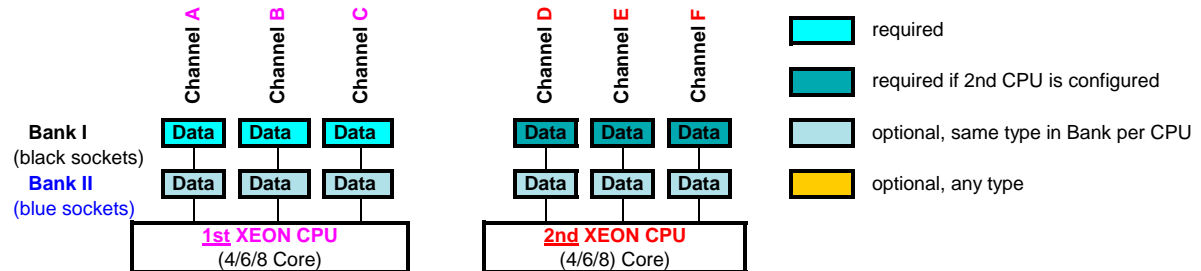
Independent Channel Mode allows all channels to be populated in any order
Can run with differently rated DIMMs and use the settings of the slowest DIMM installed in the system

2. Mirrored Channel Mode



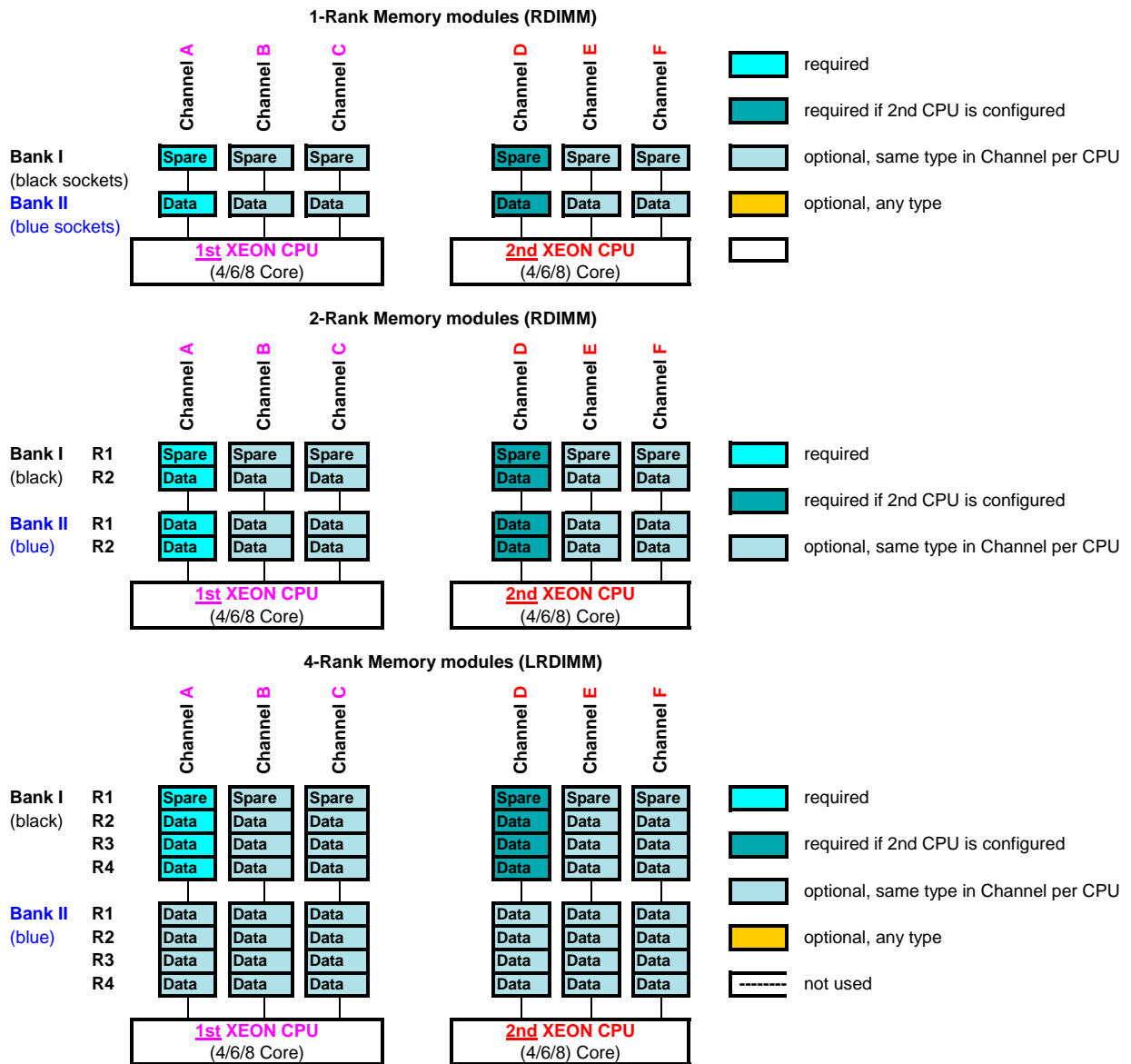
Mirrored Channel Mode requires identical modules on channel A / B (1st CPU) or channel D / E (2nd CPU)
50% of the capacity is used for the mirror => the available memory for applications is only half of the installed memory
If this mode is used, a multiple of 2 identical modules has to be ordered.

3. Performance Channel Mode



Performance Channel Mode requires identical modules on all channels of each Bank per CPU.
If this mode is used, a multiple of 3 identical modules has to be ordered.

4. Rank Sparing Mode



Rank Sparing Mode requires identical modules (same capacity and technology) within the same channel.
The available memory for applications will vary depending on configuration. Please refer to the spreadsheet above
"Effective Memory capacity with active Rank Sparing Mode". Population rule for Rank sparing mode is to achieve max.
available memory.

Change Report

[illegible]