

SE867-AGPS Evaluation Board User Guide

1VV0300864 Rev. 0 – 2009-11-06



Applicability Table

This document is relating to the following products:

PRODUCT
SE867-AGPS EVB



Contents

1. Introduction	5
1.1. Audience	5
1.2. Contact Information, Support	5
1.3. Text Conventions	6
1.4. Related Documents	6
1.5. Document change log	6
2. Overview	7
3. General product description	8
3.1. Main features	8
3.2. Board overview	9
3.2.1. Top Side	9
3.2.2. Bottom side	10
3.3. Content of the kit	11
4. DIP switches configuration	12
4.1. Numbering and symbols	12
4.2. Configuration 1	13
4.3. Configuration 2	15
4.4. Configuration 3	17
4.4.1. Option A (interfaces at 3V)	17
4.4.2. Option B (interfaces at 1.8V)	19
5. Board connection and usage	21
5.1. Connections and start-up	21
5.2. Usage	21



1. Introduction

The information presented in this document is believed to be accurate and reliable. However, no responsibility is assumed by Telit Communications S.p.A. for its use, nor any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent rights of Telit Communications S.p.A. other than for circuitry embodied in Telit products. This document is subject to change without notice.

1.1. Audience

This document is intended for customers who are evaluating one or more products in the applicability table.

1.2. Contact Information, Support

For general contact, technical support, to report documentation errors and to order manuals, contact Telit Technical Support Center (TTSC) at:

TS-EMEA@telit.com

TS-NORTHAMERICA@telit.com

TS-LATINAMERICA@telit.com

TS-APAC@telit.com

Alternatively, use:

<http://www.telit.com/en/products/technical-support-center/contact.php>

For detailed information about where you can buy the Telit modules or for recommendations on accessories and components visit:

<http://www.telit.com>

To register for product news and announcements or for product questions contact Telit's Technical Support Center (TTSC).

Our aim is to make this guide as helpful as possible. Keep us informed of your comments and suggestions for improvements.

Telit appreciates feedback from the users of our information.





3. General product description

3.1. Main features

The SE867-AGPS evaluation board main features are:

- Mini-USB connector for data transfer and power supply
- FTDI chip for UART to USB conversion
- SMA connector for use with an external active antenna or connection to an instrument
- On board passive antenna plus LNA to be used as an alternative to the external active antenna
- DIP switches for configuration selection
- On-board power-on-reset and manual reset button

The on-board FTDI chip is used for UART to USB connection allowing the use of simple mini-USB cable for data transfer in place of a RS232 connection. The USB connection is exploited also to power the module avoiding the need for an external power supply and increasing the handiness of the kit (important for field tests).

The double option for the antenna (active external or passive internal + LNA) gives the possibility to test the two different antenna configurations that customers could implement in their design.



3.2. Board overview

3.2.1. Top Side

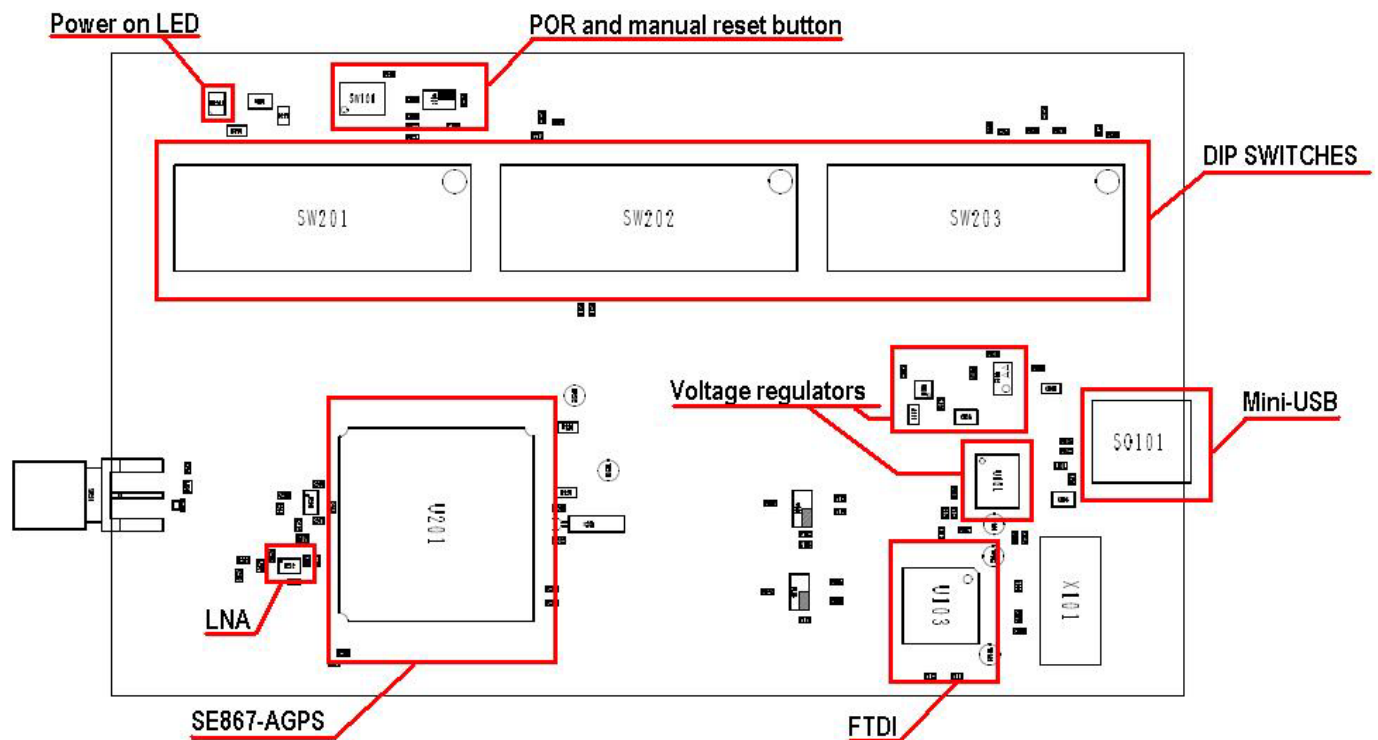
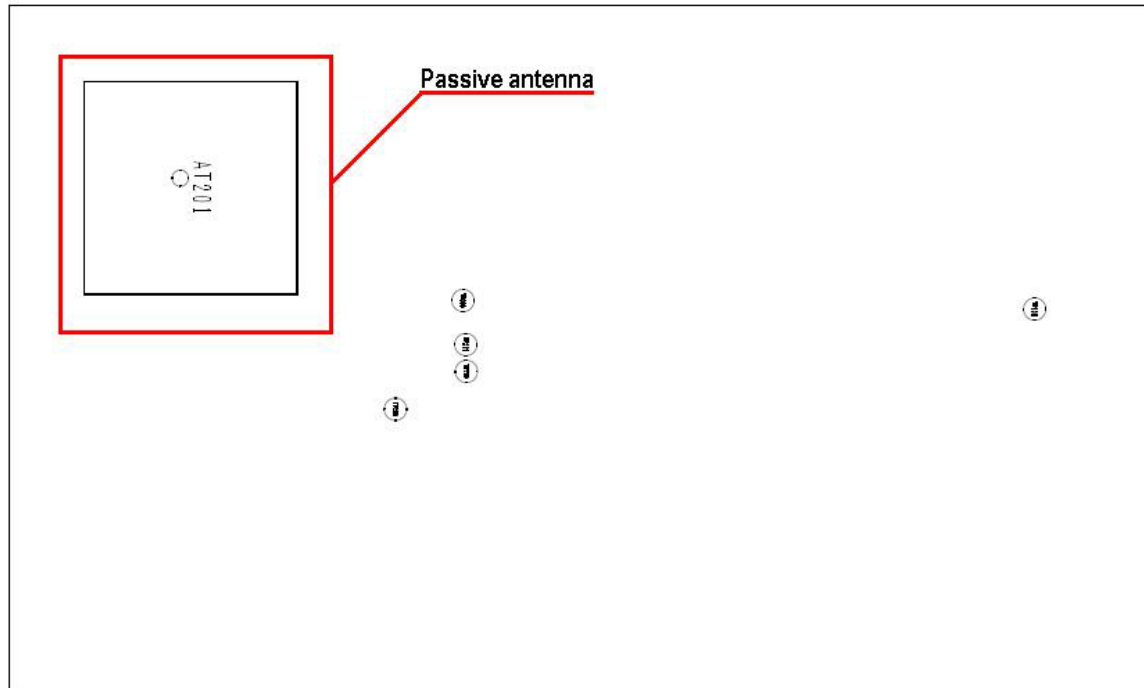


Figure 1





3.3. Content of the kit

Please check the content of your SE 867 EVB kit, if any of the items is missing, please contact your supplier.

Description	Quantity
EVB Motherboard	1
ASSEMBLED USB-A- mini USB-B CABLE	1
RED & BLACK CABLE WITH PLUGS L-60cm	1



4. DIP switches configuration

4.1. Numbering and symbols

This chapter explains with images and tables the possible switches configuration for a correct use of the board.

The images will show the switches positions and numbering in reference to the placement in the board (so that the “up” and “down” status of the switch are univocally referred to a precise point of the board).

Please note that the numbers assigned in this chapter differs from the numbers reported on the switch package in order to give continuity to the numbering and make clearer the configurations description.

The symbols used to describe the switch position are:




	Switch in UP position (the actuator is in the black area)
	Switch in DOWN position (the actuator is in the black area)
	Switch can be put either in up or down position depending on the customer needs (see tables notes for details)

Table 1



4.2. Configuration 1

In the following image and table it's reported the switch configuration necessary to work with the power configuration 1 described in the "SE867-AGPS User guide" (paragraph 4.1.1).



IMPORTANT: switch 15 controls the enable of the internal 1V8_DIG regulator. It's important to keep it in DOWN position (as showed in the figure) as long as switch 4 (V_IO reference voltage selection) is in DOWN position. Disabling the 1V8_DIG regulator while the V_IO is enabled could cause improper internal biasing of the module (as indicated also in the user guide, paragraph 4.1.4). If it's necessary disabling the 1V8_DIG switch 4 must be placed in UP position before putting switch 15 in UP position as well.

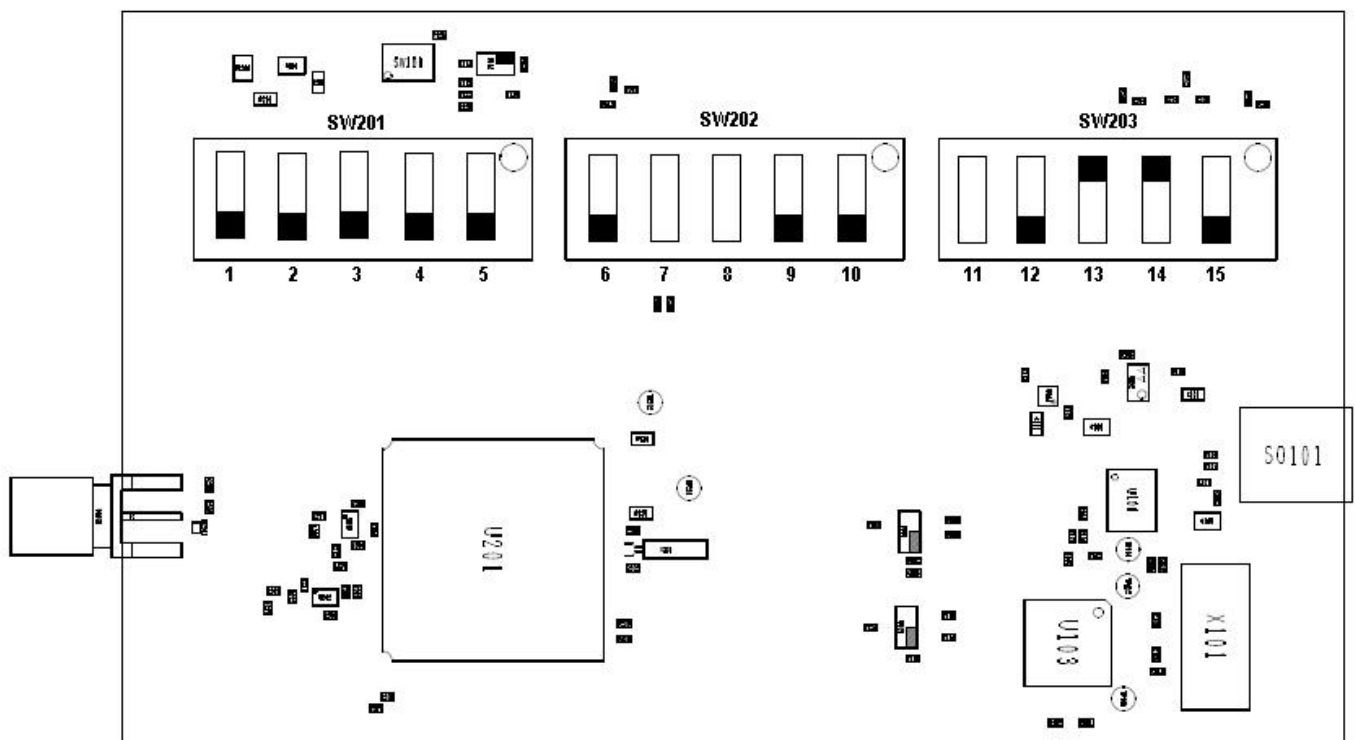


Figure 3



SE867-AGPS Evaluation Board User Guide

1VV0300864 Rev. 0 – 2009-11-06

Switch	Position	Notes
1	DOWN	
2	DOWN	
3	DOWN	
4	DOWN	
5	DOWN	
6	DOWN	
7	-	Antenna selection. UP for internal antenna. DOWN for external antenna.
8	-	Bootsel control. DOWN during normal working. UP to perform module flashing.
9	DOWN	
10	DOWN	
11	-	Unconnected switch
12	DOWN	
13	UP	
14	UP	
15	DOWN	

Table 2



4.3. Configuration 2

In the following image and table it's reported the switch configuration necessary to work with the power configuration 2 described in the "SE867-AGPS User guide" (paragraph 4.1.2).



NOTE: in this configuration V_IO is connected to 1V8_DIG (switch 4 in UP position), so it's possible to disable the 1V8_DIG regulator without risk of improper biasing.

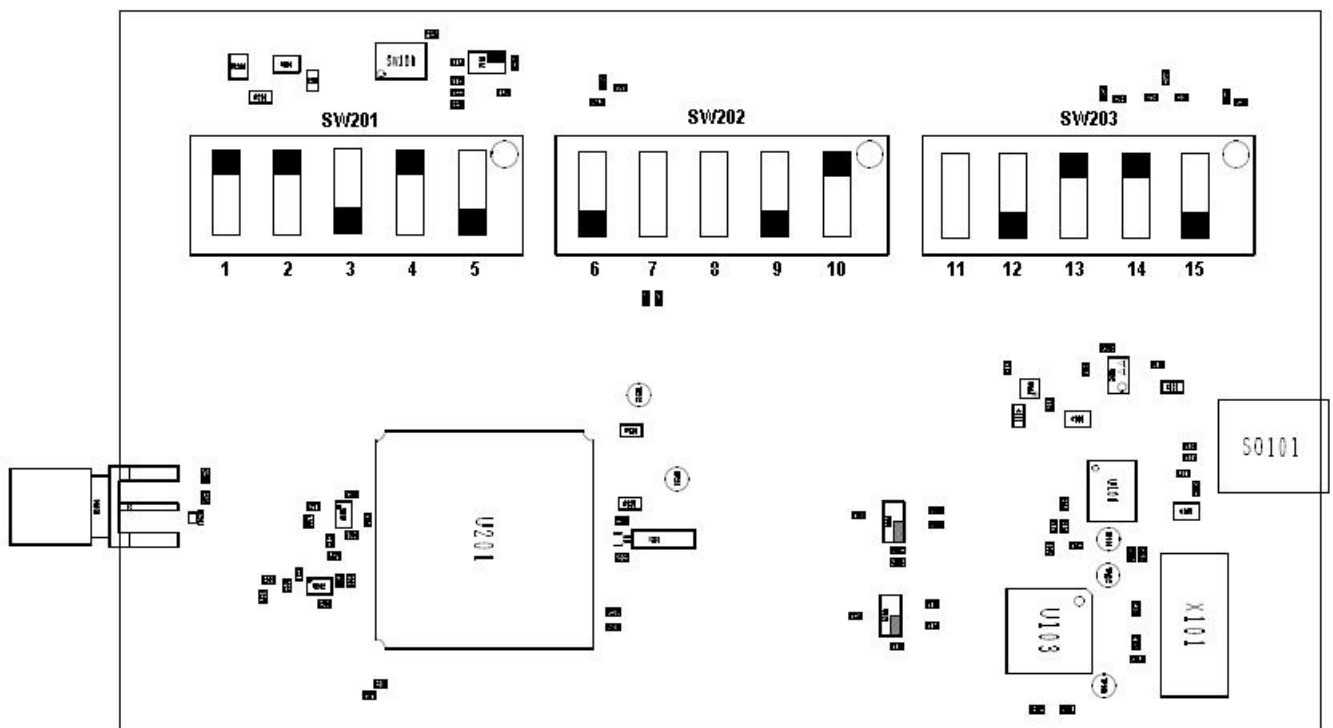


Figure 4



SE867-AGPS Evaluation Board User Guide

1VV0300864 Rev. 0 – 2009-11-06

Switch	Position	Notes
1	UP	
2	UP	
3	DOWN	
4	UP	
5	DOWN	
6	DOWN	
7	-	Antenna selection. UP for internal antenna. DOWN for external antenna.
8	-	Bootsel control. DOWN during normal working. UP to perform module flashing.
9	DOWN	
10	UP	
11	-	Unconnected switch
12	DOWN	
13	UP	
14	UP	
15	DOWN	

Table 3



4.4. Configuration 3

In the following image and table it's reported the switch configuration necessary to work with the power configuration 2 described in the "SE867-AGPS User guide" (paragraph 4.1.2). This configuration allows two different options for the interfaces logic levels: 3V and 1.8V.

4.4.1. Option A (interfaces at 3V)



IMPORTANT: also in this configuration attention must be paid in keeping 1V8_DIG enabled (in this case this voltage is externally generated and enabled by switch 13 in DOWN position, as showed in the figure) as long as switch 4 (V_IO reference voltage selection) is in DOWN position.

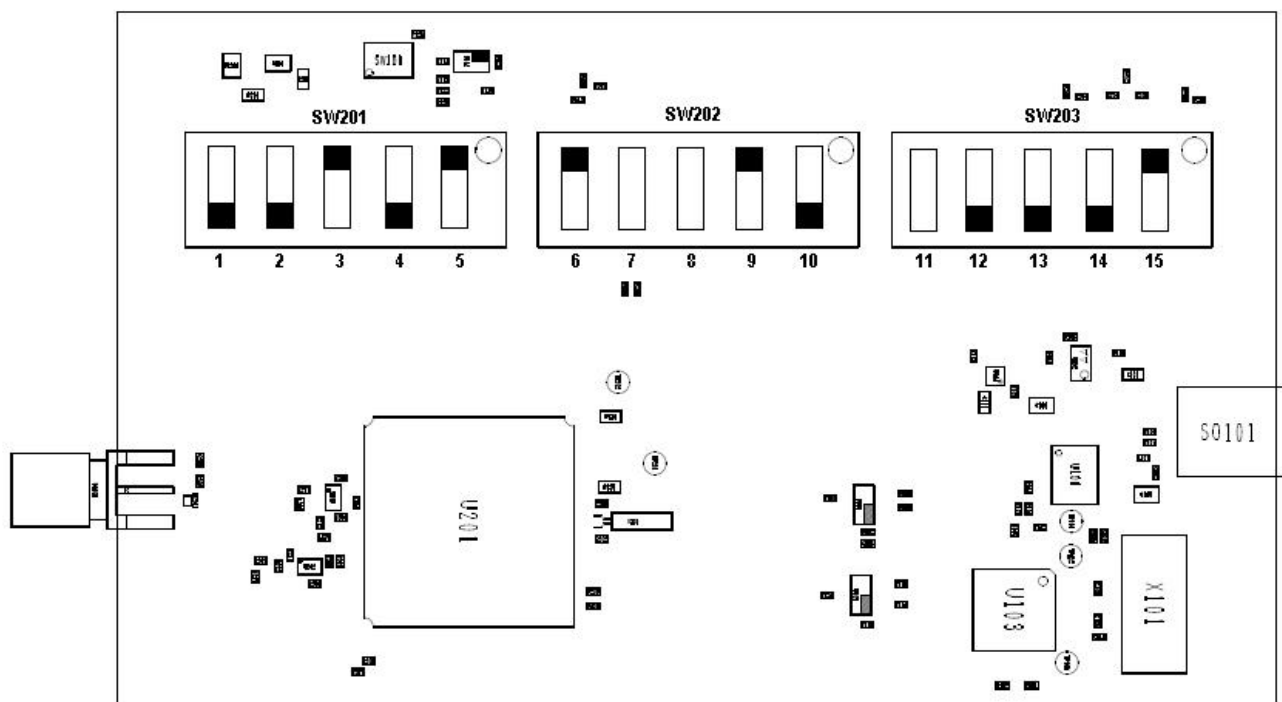


Figure 5

SE867-AGPS Evaluation Board User Guide

1VV0300864 Rev. 0 – 2009-11-06

Switch	Position	Notes
1	DOWN	
2	DOWN	
3	UP	
4	DOWN	
5	UP	
6	UP	
7	-	Antenna selection. UP for internal antenna. DOWN for external antenna.
8	-	Bootsel control. DOWN during normal working. UP to perform module flashing.
9	UP	
10	DOWN	
11	-	Unconnected switch
12	DOWN	
13	DOWN	
14	DOWN	
15	UP	

Table 4



4.4.2. Option B (interfaces at 1.8V)



NOTE: in this configuration V_IO is connected to 1V8_DIG (switch 4 in UP position), so it's possible to disable the 1V8_DIG regulator without risk of improper biasing. 1V8_DIG is still externally generated and enabled by switch 13.

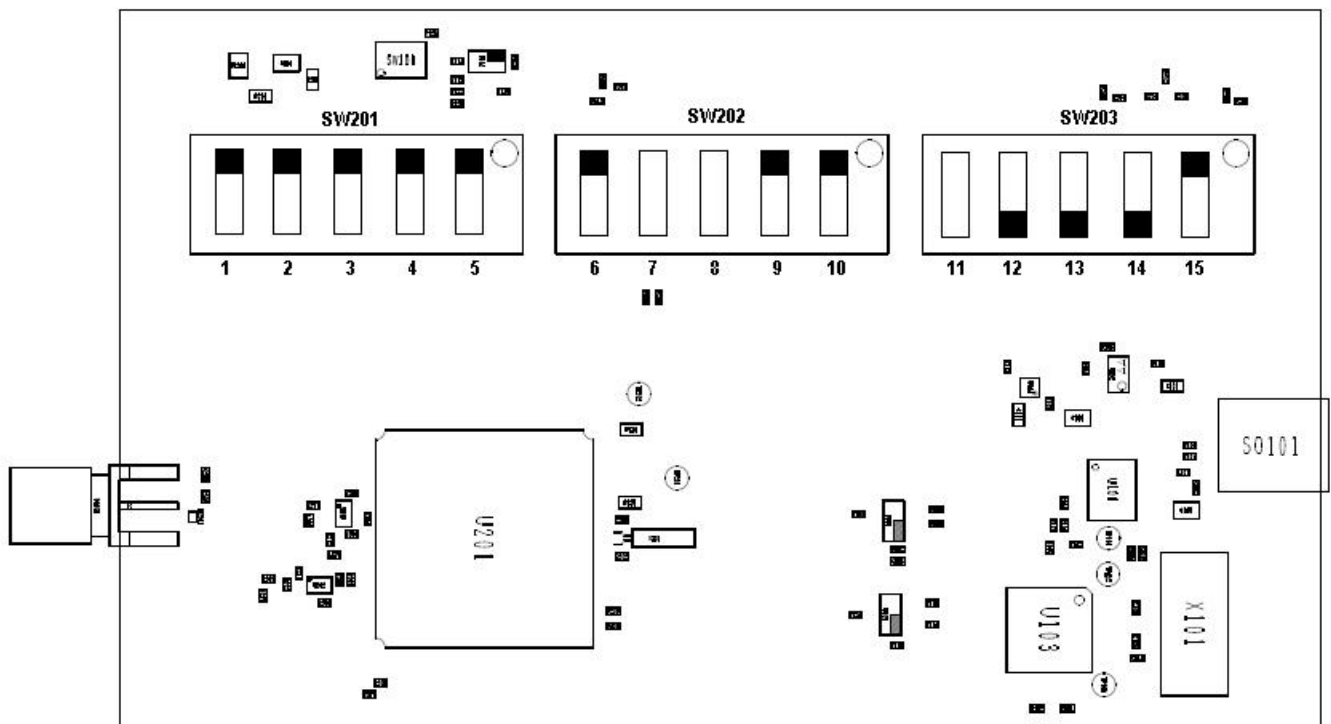


Figure 6

SE867-AGPS Evaluation Board User Guide

1VV0300864 Rev. 0 – 2009-11-06

Switch	Position	Notes
1	UP	
2	UP	
3	UP	
4	UP	
5	UP	
6	UP	
7	-	Antenna selection. UP for internal antenna. DOWN for external antenna.
8	-	Bootsel control. DOWN during normal working. UP to perform module flashing.
9	UP	
10	UP	
11	-	Unconnected switch
12	DOWN	
13	DOWN	
14	DOWN	
15	UP	

Table 5



