

# Internal Photos of EUT

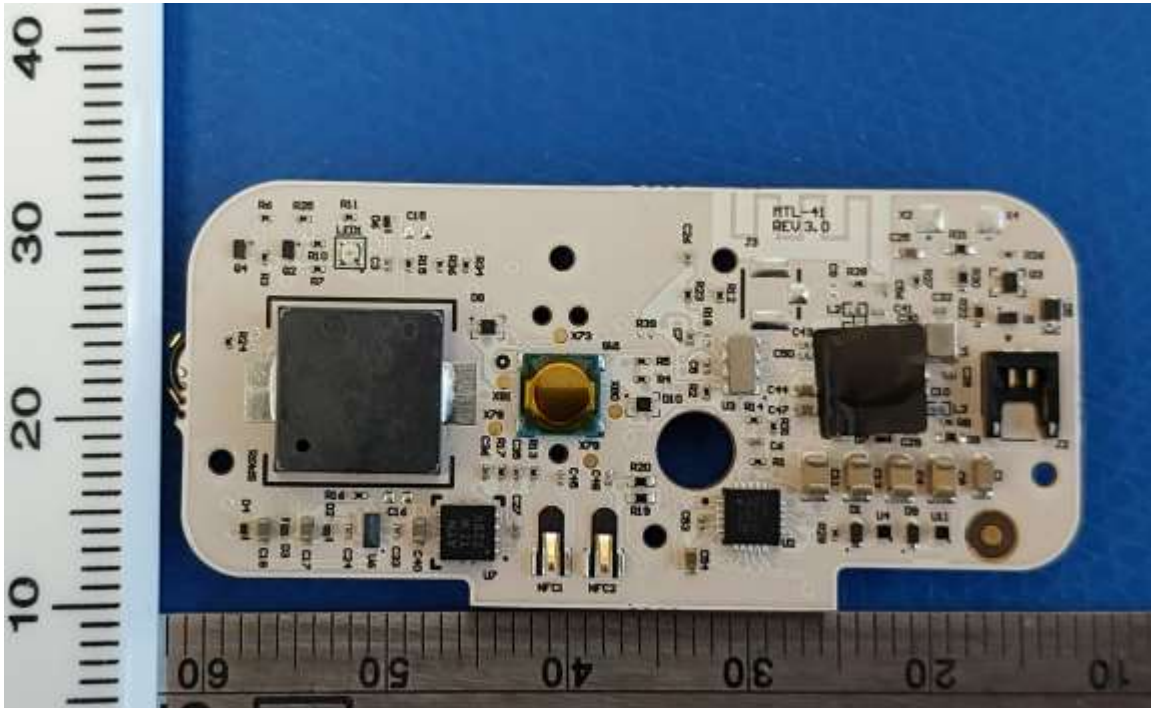


Fig 1 PCBA view 1

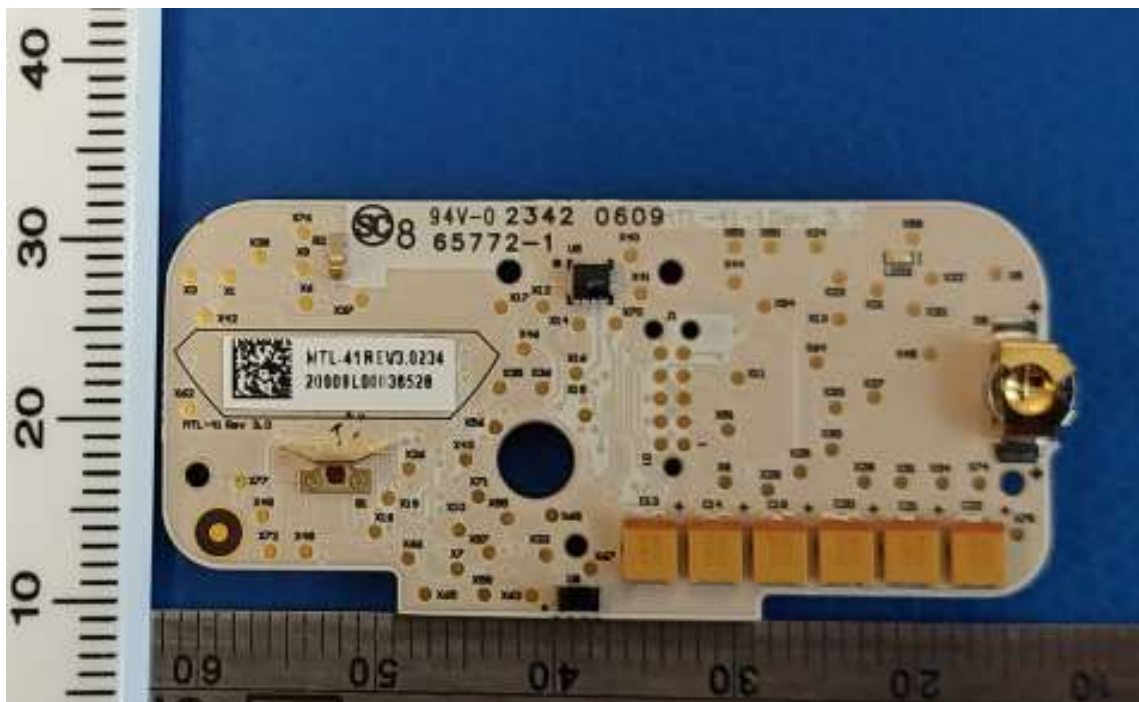


Fig 2 PCBA view 2



Fig 3 PCBA placement in host