

A AIN THE ENTERPRISE: THE INTEL® AI BUILDERS SHOWCASE

BRIGITTE ALEXANDER, MANAGING DIRECTOR AI PARTNER PROGRAMS INTEL AI PRODUCTS GROUP **SEPTEMBER 10, 2019**

TODAY'S LINE UP

- Intel® AI Builders Program
- Why Intel AI?
- Intel Al Builders Partner Showcase
- How to Deploy on Intel Architecture
- After party!
- Match-making w/ Partners



































INTEL® AI BUILDERS IS A THRIVING ENTERPRISE ECOSYSTEM

250+ Enterprise Partners with 100+ Optimized AI Solutions on Intel Technologies

Vertical Partners



Horizontal Partners



INTEL® AI BUILDERS BENEFITS FOR PARTNERS

Partner Activation

Available to all partners



- Solution definition
- Technical support
- Intel® Al Dev Cloud for Builders
- Neural Compute Stick 2
- Learning resources

Partners demonstrating solutions on Intel Al



CO-MARKETING

- Event demos & speakerships
- Intel Al Builders **Document Library**
- Digital content/ social channels

Partners optimized on Intel AI



MATCH-MAKING

Match-making of optimized partners with Intel enterprise customers

Select start ups



INVESTMENT

Intel Capital consideration by our dedicated AI investment team



INTEL® AI BUILDERS CO-MARKETING

Events demos, sessions, & keynote walk-ons



Global PR stories including TV coverage





Blogs, white papers & solution briefs highlighting AI optimizations on Intel







Weekly podcasts



Partner videos



Partner advocates for Intel® AI and the Intel® AI Builders Program

"Intel pla world-ci learning with Intel develop Builders in order further."

world-class algorithms are written using interls Math Kernel Liberies, Interlo elearing toolkir (Depertion*), Interlo multitreaded bildraies and are accelerated with interls Xeen Scalable Servers. As we continue to advance our platform and develop additional Al healthcare applications, we are confident that intel Al Builders will deliver the robust hardware and data management systems needed in order to push the standard of care for best-in-class diagnostics solutions ever

► ROBERT MEHLER Chief Operating Officer, MaxQ

Social amplification



Partner microsites



Analyst days, press briefings and Intel Capital summits

Intel® AI Builders newsletters



INTEL® AI BUILDERS BENEFITS FOR ENTERPRISE END USERS

Road Map Alignment Engagement and Support from Planning to Deployment

Faster and Safer Deployments

CONNECT

with preferred solution, providers, get access to state of the art solutions and benefit from best practices



TEST

your roadmap of interoperable, scalable, and flexile solutions by utilizing the resources available only to Intel® AI Builders members



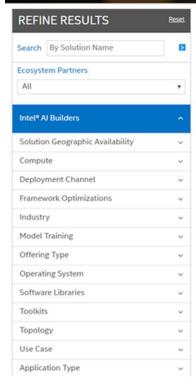
DEPLOY

optimized, tested, and reliable solutions, and continue to drive the transformation of your system



SOLUTIONS CATALOG

Find optimized and market-ready partner solutions to support your AI deployment needs

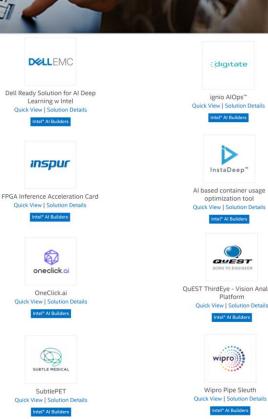
















Intel® Al Builders

WE INVITE YOU TO LEARN MORE ABOUT INTEL AI BUILDERS...

- Go to builders.intel.com/ai
- See more Intel® AI Builders in the Intel booth tomorrow & Thurs:

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PARTNER WITH INTEL® TO ACCELERATE YOUR AI JOURNEY



HARDWARE Multi-purpose to purpose-built AI compute from cloud to device



MAINSTREAM































INTEL® XEON® PROCESSORS

Now Optimized For Deep Learning

INFERENCE THROUGHPUT



Intel® Xeon® Platinum 8180 Processor
higher Intel optimized Caffe GoogleNet v1 with Intel® MKL
inference throughput compared to
Intel® Xeon® Processor E5-2699 v3 with BVLC-Caffe

Inference and training throughput uses FP32 instructions

TRAINING THROUGHPUT



Intel® Xeon® Platinum 8180 Processor
higher Intel Optimized Caffe AlexNet with Intel® MKL
training throughput compared to
Intel® Xeon® Processor E5-2699 v3 with BVLC-Caffe



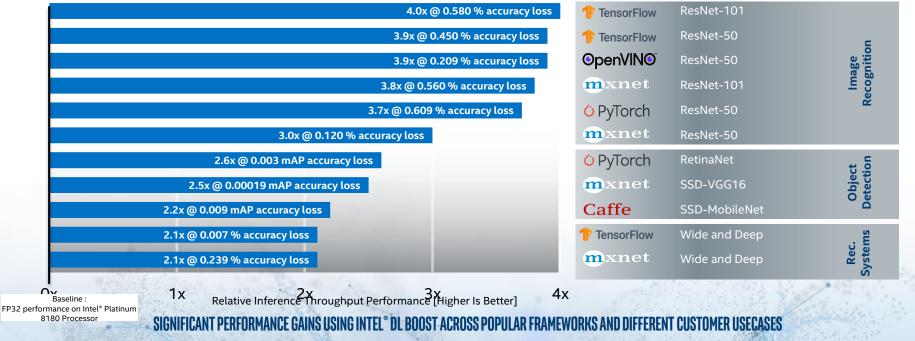
Deliver significant AI performance with hardware and software optimizations on Intel® Xeon® Scalable Family

1 Performance results are based on testing as of June 2018 and may not reflect all publicly available security updates. Configurations: See slide 21822. For more complete information about performance and benchmark results, visit www.intel.com/benchmarks

ENABLING DEEP LEARNING USE CASES WITH INTEL® DEEP LEARNING BOOST

2S Intel® Xeon® Platinum 8280 Processor vs 2S Intel® Xeon® Platinum 8180 Processor





Performance results are based on testing as of 3/26/2019 and may not reflect all publicly available security updates. Configurations: See slide 218.22. For more complete information about performance and benchmark results, visit www.intel.com/benchmarks. These optimizes to manufactured by the results of t

SOFTWARE: THE BEST "OUT-OF-THE-BOX" AI EXPERIENCE

SOFTWARE FRAMEWORKS

CONTAINERS

CLOUD ENVIRONMENTS







- 1. Frameworks: All Intel optimizations up-streamed to https://github.com/tensorflow/tensorflow
- 2. Anaconda: conda install -c anaconda tensorflow; conda install -c intel tensorflow
- 3. Docker: \$ docker pull intelaipg/intel-optimized-tensorflow:1.10.0
- 4. Amazon: https://www.intel.ai/amazon-web-services-works-with-intel-to-enable-optimized-deep-learning-frameworks-on-amazon-ec2-cpu-instances
- 5. GCP:
- 6. Microsoft Azure:

ECOSYSTEM

- Global
- Cross-industry
- All Al use cases
- Intel optimized
- Market-ready solutions



FOSTERING THE NEXT GENERATION OF AI

The intel® A Buildon program is an enterprise exception of inclusing leading independent software whom is [Seld, system integration [Seld, adignal equipment manufactures [SOH], and enterprise end users who have a continued exception of artificial intelligence areas into profession. The intel® is profession broades in whom a establishment includes to a continue and an explaint and an explaint and intelligence areas into profession and intelligence areas into profession and intelligence areas in a profession and intellige



PUBLIC

Configuration for Intel® DL Boost Performance Gains over FP32 on Xeon®

4.0x performance boost with TensorFlow ResNet101: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1.el7.elrepo.x86_64, SSD 1x INTEL SSDSCZKG96 960GB, Deep Learning Framework: TensorFlow: <a href="https://hub.docker.com/r/intelaipg/intel-optimized-tensorflow/ten

3.9x performance boost with TensorFlow ResNet50: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/ 32GB/ 2933 MHz), BIOS: SE5C620.86B.0D.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1.47.eirepo.x86_64, SSD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: TensorFlow: Intels://hub.docker.com/r/intelaipg/intel-optimized-tensorflow:PR25765-devel-mkl (https://github.com/lensorflow/resnet50) commit: 87261e70a902513f934413f0093464472eed6642, SD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: TensorFlow: 1 https://github.com/lensorflow/resnet50 commit: 87261e70a902513f934413f0093464472eed6642, SD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: TensorFlow: https://hub.docker.com/r/intelaipg/intel-optimized-tensorflow/resnet50 commit: 87261e70a902513f934413f009364c4f2eed6642, SD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: TensorFlow: https://hub.docker.com/r/intelaipg/intel-optimized-tensorflow/resnet50 commit: 87261e70a902513f934413f009364c4f2eed6642, Spithetic data, Batch Size=128, 2 instance/2 socket, Datatyoe: F932

3.9x performance boost with OpenVino™ ResNet-50: Tested by Intel as of 1/30/2019. 2 socket Intel® Xeon® Platinum 8280 Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0271.120720180605 (ucode:0x4000013), Linux-4.15.0-43-generic-x86. 64-with-debian-buster-sid, Compiler: gcc (Ubuntu 7.3.0-27ubuntu1-18.04) 7.3.0, Deep Learning ToolKit: OpenVINO R5 (DLDTK Version:1.0.19154 , AIXPRT CP (Community Preview) benchmark (https://www.principledtechnologies.com/benchmarkxpt/aixpt/) BS=64, Imagenet images, 1 instance/2 socket, Datatype: INT8 vs Tested by Intel as of 1/30/2019. 2 socket Intel® Xeon® Platinum 8180 Processor, 28 cores HT On Turbo ON Total Memory 192 GB (12 slots/16GB/2633 MHz), BIOS: SE5C620.86B.0D.01.0271.120720180605, Linux-4.15.0-29-generic-x86_64-with-Ubuntu-18.04) 7.3.0, Deep Learning ToolKit: OpenVINO R5 (DLDTK Version:1.0.19154), AIXPRT CP (Community Preview) benchmark (https://www.principledtechnologies.com/benchmarkxpt/aixpt/) BS=64, Imagenet images, 1 instance/2 socket, Datatype: FP32

3.8x performance boost with MXNet ResNet101: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1.el7.eirepo.x86.64, SSD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: MXNet https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet101: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Donol 1.0286.121520181757 (ucode:0x5000057), CentOS 7.6, Kernel 4.19.5-1.el7.eirepo.x86.64, SSD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: MXNet https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet101: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet101: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet101: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet101: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582/261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet101: https://github.com/apache/incubator

3.7x performance boost with PyTorch ResNet50: Tested by Intel as of 2/25/2019. 2 socket Intel® Xeon® Platinum 8280 Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0271.120720180605 (ucode: 0x4000013), Ubuntu 18.04.1 LTS, kernel 4.15.0-45-generic, SSD 1x sda INTEL SSDSC2BA80 SSD 745.2GB, 3X INTEL SSDPE2KX040T7 SSD 3.7TB, Deep Learning Framework: Pytorch with ONNX/Caffe2 backend: https://github.com/pytorch/pytor

PUBLIC

Configuration for Intel® DL Boost Performance Gains over FP32 on Xeon® (cont.)

performance boost with MXNet ResNet50: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/ 32GB/ 2933 MHz), BIOS: SE5C620.86B.0D.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1.el7.elrepo.x86_64, SSD x INTEL SSDSCZKG96 960GB, Deep Learning Framework: MXNet https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, ResNet50: https://github.com/apache/incubator-mxnet.git -b master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version

2.5x Performance boost with MXNet SSD-VGG16 Inference: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1.el7, elrepo.x86_64, SSD 1x INTEL SSDSC2KG96 960GB, Deep Learning Framework: MXNet https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1, MKL DNN version: v0.17, SSD-VGG16: https://github.com/apache/incubator-mxnet.git + master da5242b732de39ad47d8ecee582f261ba5935fa9, Compiler: gcc 6.3.1,

2.2x performance boost with Intel® Optimized Caffe SSD-Mobilenet v1: Tested by Intel as of 2/20/2019. 2 socket Intel® Xeon® Platinum 8280 Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0271.120720180605 (ucode: 0x4000013), Ubuntu 18.04.1 LTS, kernel 4.15.0-45-generic, SSD 1x sda INTEL SSDSC2BA80 SSD 745.2GB, Deep Learning Framework: Intel® Optimization for Caffe version: 1.1.3 (commit hash: 7010334f159da247db3fe3a9d96a3116ca06b09a), ICC version 18.0.1, MKL DNN version: v0.17 (commit hash: 830a10059a018cd2634d94195140cf2d8790a75a), model: https://github.com/intel/(caffe/blob/master/models/intel/sod mobilenet_ints.prototxt, Synthetic Data, Batch Size=64, 2 instance/2 socket, Datatype: INT8 vs Tested by Intel as of 2/21/2019. 2 socket Intel® Xeon® Platinum 8180 Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0271.120720180630 (ucode: 0x200004), Usual National National

2.6x performance boost with PyTorch RetinaNet: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280 Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/32GB/2933 MHz), BIOS: SE5C620.86B.0D.01.0271.120720180605 (ucode: 0x4000013), Ubuntu 18.04.1 LTS, kernel 4.15.0.45-generic, SSD 1x sda INTEL SSDSC2ZBA80 SSD 745.2GB, 3X INTEL SSDPEZKX04017 SSD 3.7TB, Deep Learning Framework: Pytorch with ONNX/Caffe2 backend: https://github.com/pytorch/pyt

2.1x performance boost with TensorFlow Wide & Deep: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/ 32GB/ 2933 MHz), BIOS: SE5C620.86B.DD.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1.el7 elrepox.86. 64, SSD 1x INTEL SSDSCZKG96 960GB, Deep Learning Framework: TensorFlow <a href="https://sibrage.googleapis.com/intel-Jobinized-lensorflow/models/tree/master/benchmarks/recompt-lensorflow/models/tree/master/benchmarks/recompt-lensorflow/models/tree/master/benchmarks/recompt-lensorflow/models/tree/master/benchmarks/recompt-lensorflow/models/tree/master/benchmarks/recompt-lensorflow/models/tensorflow/mod

https://github.com/IntelA/models/tree/master/benchmarks/recommendation/tensorflow/wide_deep_large_ds_commit: a044cb3e7d2b082aebae2edbe6435e57a2cc1f8f, Model: https://storage.googleapis.com/intel-optimized-tensorflow/models/wide_deep_int8 pretrained model.pb, Dataset: Criteo Display Advertisement Challenge, Batch Size=512, 1 instance/1 socket, Datatype: FP32

2.1x performance boost with MXNet Wide & Deep: Tested by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/ 32GB/ 2933 MHz), BIOS: SE5C620.86B.0D.01.0348.011820191451 (ucode:0x5000017), CentOS 7.6, Kernel 4.19.5-1el7 elrepox.86, 64, SSD 1x INTEL SSDSCZKG96 960CB, Deep Learning Framework: MXNet by Intel as of 3/26/2019. 2 socket Intel® Xeon® Platinum 8280L Processor, 28 cores HT On Turbo ON Total Memory 384 GB (12 slots/ 32GB/ 2933 MHz), BIOS: SE5C620.86B.0D.01.02fmxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wide_deep_criteo/oncom/intel/loptimized-models/blob/v1.0.2/mxnet/wi

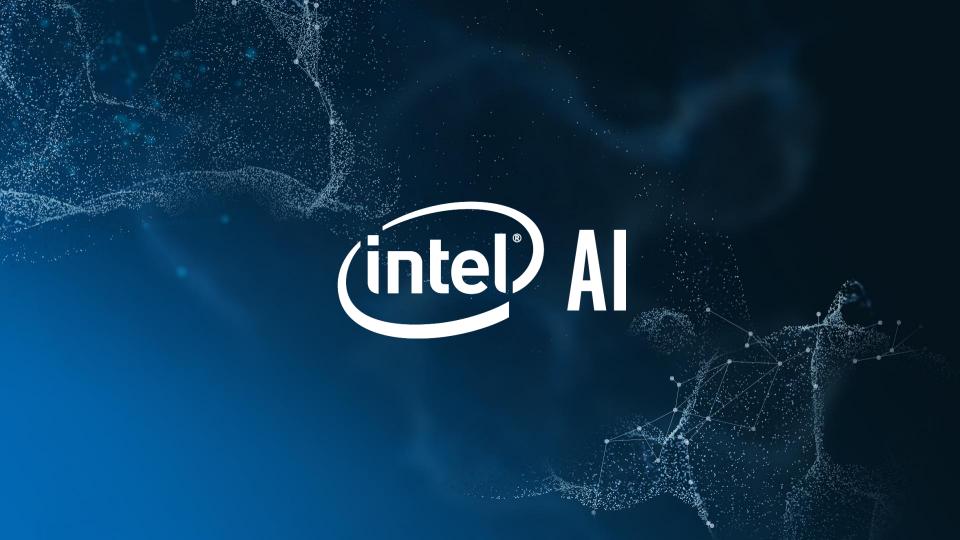


SHOWCASE SCHEDULE

Meeting rooms are available to meet with presenting partners throughout the event.

Time	Session	Speaker
1:30pm	Welcome and AIB overview and growth: Impact on AI ecosystem	Brigitte Alexander (Intel)
1:40pm	Driving business impact with the Intel AI technology portfolio	Ananth Sankaranarayanan (Intel)
2:00pm	Clinical deployment of radiology Al powered by OpenVINO	Liren Zhu (Subtle Medical)
2:10pm	Industrialize AI with Cloudera	Jessie Lin (Cloudera)
2:20pm	Automated time series forecasting and optimization for enterprises	Yuan Shen (OneClick.ai)
2:30pm	QuEST vision analytics solution with OpenVINO and Intel AI	Rubayat Mahmud (QuEST Global)
2:40pm	Saving Antarctic penguins with deep learning	Ganes Kesari (Gramener)
2:50pm	Pipe Sleuth: Al-based pipeline assessment	Sundar Varadarajan (Wipro)

3:00-3:30pm	Afternoon break Outside meeting rooms	
3:30pm	Al-based container usage optimization tool	Amine Kerkeni (InstaDeep)
3:40pm	The turnkey high-compliance Al platform	David Talby (John Snow Labs)
3:50pm	Accelerating Al from research to production in the enterprise	Ari Kamlani (Skymind)
4:00pm	Running enterprise IT more efficiently, improving customer experience, and increasing the agility and stability of IT	Anjali Gajendragadkar (Digitate)
4:10pm	Using AI to accelerate time to customer	Derek Wang (Stratifyd)
4:20pm	Data analytics at the retail edge	Han Yang (Cisco)
4:30pm	Accelerate innovation with DevOps-like agility for machine learning pipelines	Nanda Vijaydev (Hewlett Packard Enterprise)
4:40pm	Accelerating deep learning workloads in the cloud and data centers	Ravi Panchumarthy (Intel)
5:00 PM	How to leverage powerful Intel-based instance types to create new solutions	Carlos Escapa (Amazon Web Services)
5:10 PM	Intel-based AI solutions from cloud to edge	Bob Anderson (Inspur)
5:20 PM	Lenovo intelligent computing orchestration	Matt Ziegler (Lenovo)
5:30 PM	Dell Ready Solutions	Phlip Hummel (Dell EMC)
5:40 PM	Closing Remarks	Brigitte Alexander (Intel)





AI BUILDERS SHOWCASE SUBTLE MEDICAL

AGENDA

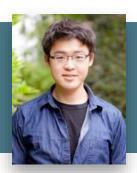


- Company overview
- Business problem they solve by prioritized vertical
- Use of Intel® AI technology
- Results
- Contact



SUBTLE MEDICAL





Founder & CEO

Enhao Gong, PhD

EE @ Stanford University

BME @ Tsinghua University

Forbes 30under30 China &Asia







Founder

Greg Zaharchuk, MD PhD

Radiologist @ Stanford Hospital

Professor @ Stanford Medicine

Director, Center of Advanced Functional Neuroimaging

Chair, ISMRM Machine Learning Workshop

- Subtle Medical Inc was founded in July 2017 from Stanford University
- First Al-powered product SubtlePET received FDA clearance and CE Mark in Nov 2018

Key Team Members



Tao Zhang, PhD Head of Research & Development







Liren Zhu, PhD
Head of Engineering







Lusi Chien, MBA
Head of Commercialization
STANFORD
BUSINESS



Praveen Gulaka, PhD Head of Clinical & Regulatory Affairs



SAMSUNG



Anna Menyhart Head of Marketing







Jorge Guzman Head of Platform





Ajit Shankaranarayanan, PhD Head of Partnership





BUSINESS PROBLEM SOLVED

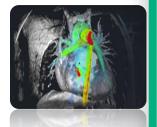


The Radiology Workflow

Scanner



Image Enhancement & Acquisition Visualization







Diagnosis



Prognosis Treatment



Most deep learning companies and applications focus here

- More frequent usage
- Immediate and bigger financial value
- Fundamental for downstream application

SUBTLE MEDICAL

90% of the cost of imaging

BUSINESS PROBLEM SOLVED



Better Economics



More patients on existing machines without additional capital purchase

Better Care



Competitive advantage: Latest technology for better quality and service



+1 patient scanned per day = approx. \$500k a year

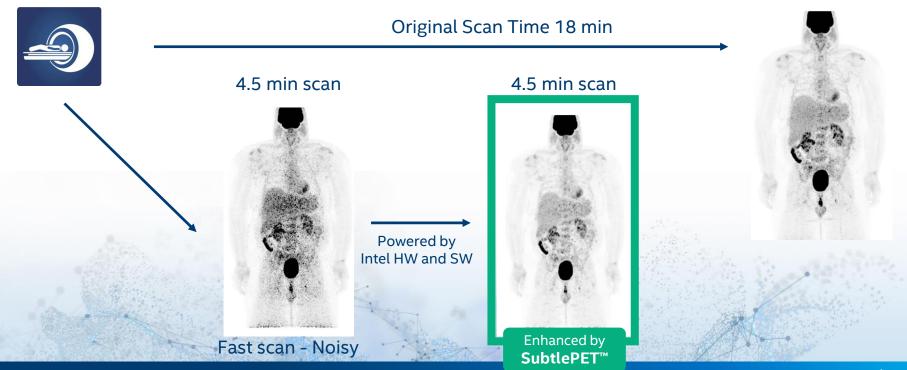


Increased patient comfort
= Higher patient satisfaction

BUSINESS PROBLEM SOLVED



SubtlePET[™] enhances up to 4x faster PET scans



USE OF INTEL® AI TECHNOLOGY



HW: Intel[®] Xeon[®] CPU E5 family, Intel[®] CoreTM i5 and i7 series

SW: Intel® Distribution of OpenVINO™ Toolkit 2019 R1.1



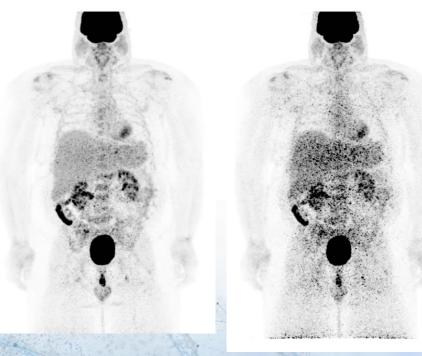
RESULTS

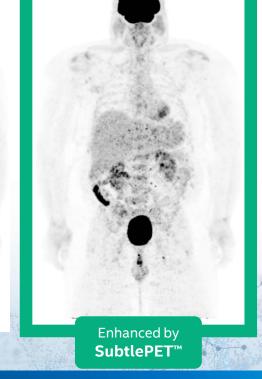


24 min scan



6 min scan

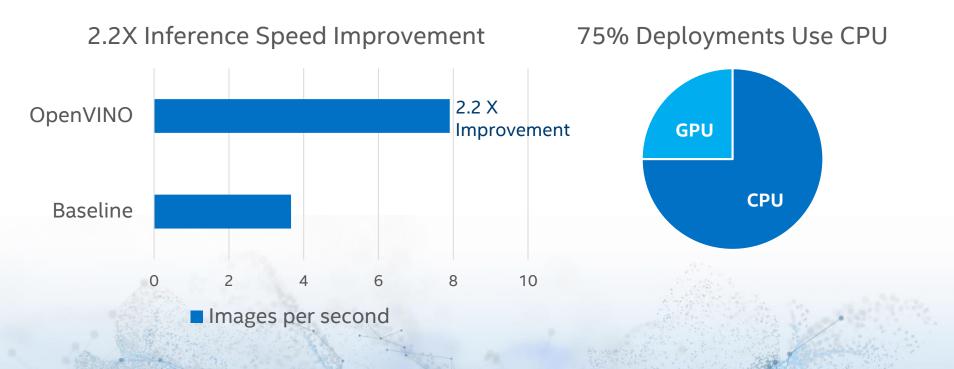




*Note: Images are Coronal Maximum Intensity Projection

RESULTS





CONFIGURATION SPECIFICATIONS

*Other names and brands may be claimed as the property of others.

Configuration: AWS p3.2xlarge instance, Intel® Xeon® E5-2686 v4 @ 2.30 GHz, 61 GB RAM, Intel® OpenVINO™ Toolkit. Testing done by Subtle Medical, Jan 2019

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors.

Performance results are based on testing as of dates shown in configuration and may not reflect all publicly available security updates. No product can be absolutely secure. See configuration disclosure for details.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit: http://www.intel.com/performance

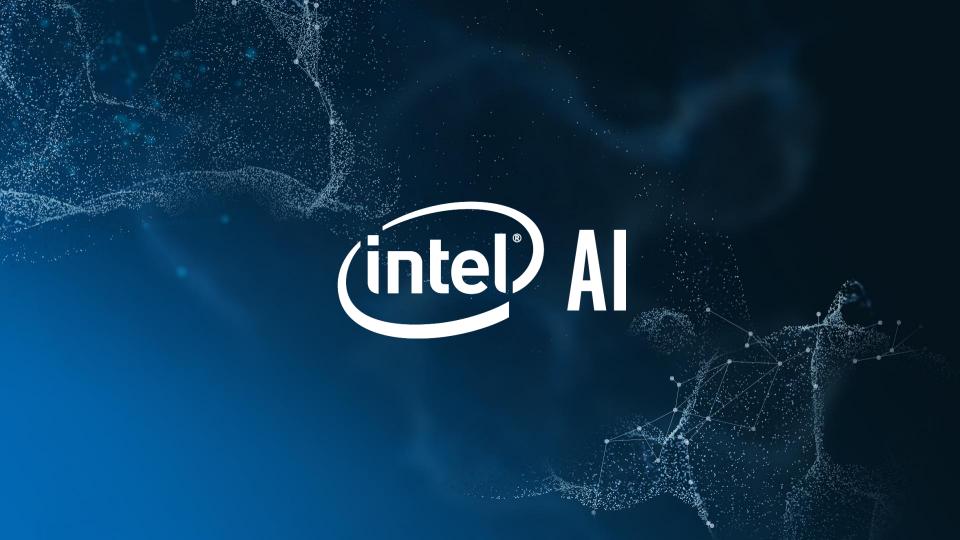
CONTACT



Liren Zhu
Head of Engineering
liren@subtlemedical.com



Visit our table with your questions, or stop by the Intel® AI Builders matchmaking table to set up a private meeting.





INTEL

AI BUILDERS SHOWCASE CLOUDERA



AGENDA

- Company overview
- Predictive Maintenance Use Case
- Use of Intel® AI technology
- Contact





MACHINE LEARNING AT CLOUDERA

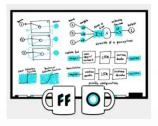
Our approach



Open **platform** to build, train, and deploy many scalable ML applications



Comprehensive data science tools to accelerate team productivity



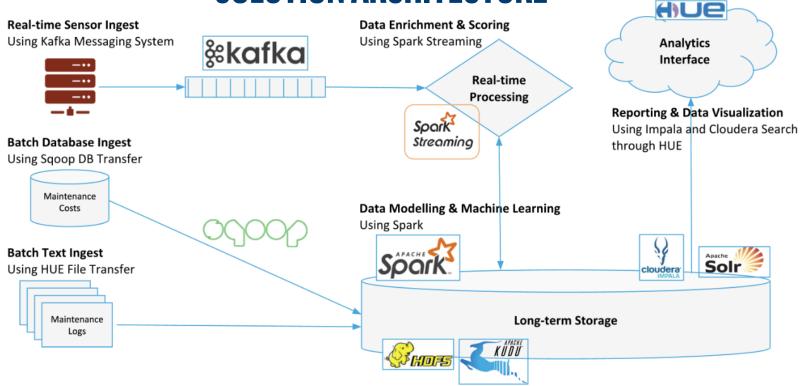
Expert guidance & services to fast track
value & scale

CLOUDERA

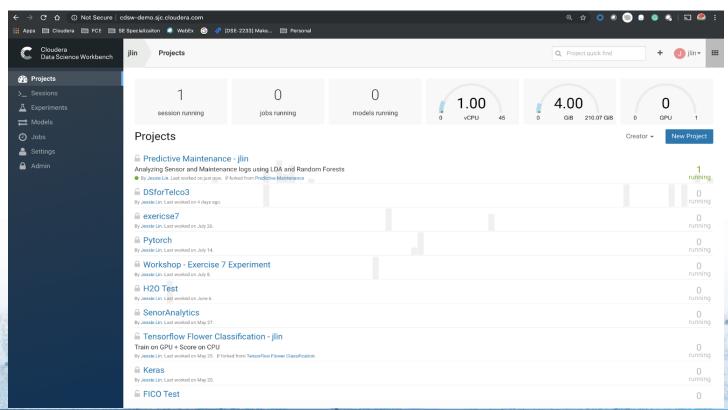
PREDICTIVE MAINTENANCE



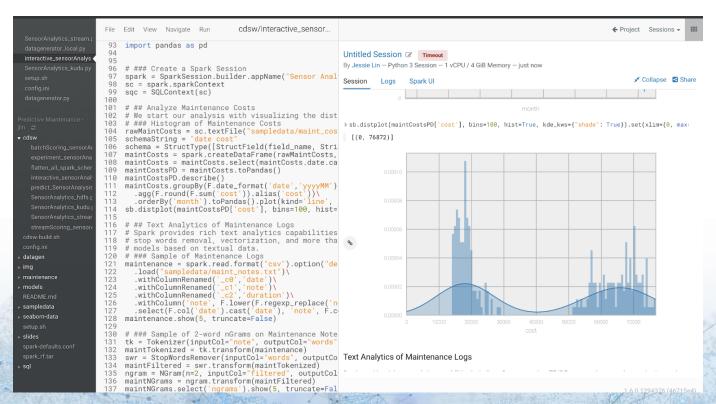
SOLUTION ARCHITECTURE



CLOUDERA DATA SCIENCE WORKBENCH



WORKBENCH EDITOR



MAINTENANCE NOTES

Sample of Maintenance Logs

```
> maintenance = spark.read.format("csv").option("delimiter", "|")\
   .load("sampledata/maint_notes.txt")\
   .withColumnRenamed('_c0', 'date')\
   .withColumnRenamed('_c1', 'note')\
   .withColumnRenamed('_c2', 'duration')\
   .withColumn('note', F.lower(F.regexp_replace('note', '[.!?-]', '')))\
   .select(F.col('date').cast('date'), 'note', F.col('duration').cast('int'))
> maintenance.show(5, truncate=False)
  Idate
  |2014-04-08|asset failure due to high sensor_6 and sensor_5 asset shutdown corrective maintenance required|22
  |2014-04-10|program maintenance tests all passed asset healthy sensor readings normal
  |2014-04-16|asset failure due to high sensor_8 and sensor_5 asset shutdown corrective maintenance required|22
  |2014-04-19|asset failure due to high sensor_2 and sensor_5 asset shutdown corrective maintenance required|18
  |2014-04-20|program maintenance tests all passed asset healthy sensor readings normal
  +-----
  only showing top 5 rows
```

Topic Clustering using Latent Dirichlet Allocation (LDA)

LDA is a form of un-supervised machine learning that identifies clusters, or topics, in the data

```
> cv = CountVectorizer(inputCol="ngrams", outputCol="features", vocabSize=50)\
    .fit(maintNGrams) # CountVectorize converts nGram array into a vector of counts
> maintVectors = cv.transform(maintNGrams)
> vocabArray = cv.vocabulary
> lda = LDA(k=3, maxIter=10)
> ldaModel = lda.fit(maintVectors)
> ldaModel.write().overwrite().save('lda.mdl')
> maint_topics=[None] * 3
> topics = ldaModel.describeTopics(5)
```

We see below that each maintenance log can be clustered based on its text into 1 of 3 topics below. The nGrams in each cluster show clearly 3 types of maintenance activities

- 1. Preventive maintenance occurs when the we have 'abnormal readings' or a 'component replacement'
- 2. Corrective maintenance occurs when we have a 'asset shutdown' event or 'asset failure'
- 3. The rest of the logs indicate that no downtime is required (ie, 'maintenance tests passed', 'asset healthy')

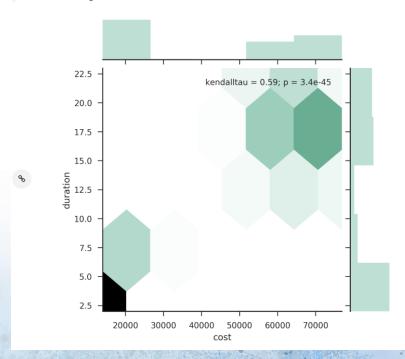
We see below that each maintenance log can be clustered based on its text into 1 of 3 topics below. The nGrams in each cluster show clearly 3 types of maintenance activities

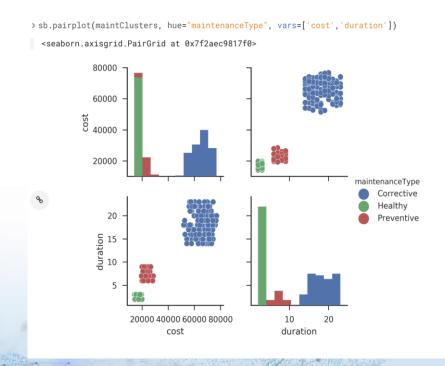
- 1. Preventive maintenance occurs when the we have 'abnormal readings' or a 'component replacement'
- 2. Corrective maintenance occurs when we have a 'asset shutdown' event or 'asset failure'
- 3. The rest of the logs indicate that no downtime is required (ie. 'maintenance tests passed', 'asset healthy')

```
> for topic in topics.collect():
     print('Topic %d Top 5 Weighted nGrams' % (topic[0]+1))
     for termIndex in topic[1]:
         print(' %s' % vocabArray[termIndex])
  Topic 1 Top 5 Weighted nGrams
   sensor readings
   tests passed
   asset healthy
   program maintenance
   readings normal
  Topic 2 Top 5 Weighted nGrams
   sensor_5 asset
   asset shutdown
   corrective maintenance
   due high
   failure due
  Topic 3 Top 5 Weighted nGrams
   showing abnormal
   abnormal readings
   component replacement
   readings preventive
   required scheduling
```

MAINTENANCE COST AND DURATION

<seaborn.axisgrid.JointGrid at 0x7f2aecc92550>



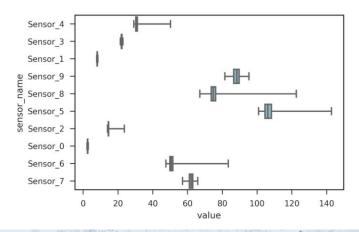


SENSOR READINGS

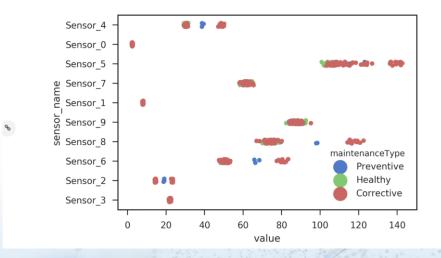
```
> sb.set(style="ticks", palette="muted", color_codes=True)
> ax = sb.boxplot(x="value", y="sensor_name",
                 data=dailyRawMeasurements.filter('value!=0 and year(day)>=2016').select('
                 whis=np.inf. color="c")
```

/usr/local/lib/python3.6/site-packages/seaborn/categorical.py:462: FutureWarning: remove _na is deprecated and is a private function. Do not use.

box_data = remove_na(group_data)







MODEL TRAINING AND EVALUATION

Model Training

We split the data into 2 subsets - one to train the model, and one to test/evaluate it We then build a pipeline of all steps involved in running the model on some data. The pipeline will have the following steps:

- 1. VectorAssembler put all features into a single column
- 2. StringIndexer convert the maintenance type strings to a numeric index
- 3. RandomForestClassifier classify the data into one of the different indexes
- 4. IndexToString convert he maintenance type indexes back to strings

Model Evaluation

The training data was used to fit the model (ie. train it), now we can test the model using the test subset, and calculate the accuracy (ie. false prediction rate)

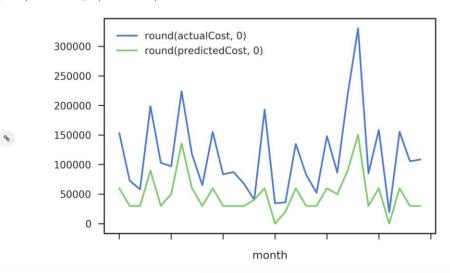
	Prediction	Actual	count
0	Corrective	Corrective	44
1	Healthy	Healthy	26
2	Corrective	Preventive	2
3	Healthy	Preventive	1
4	Preventive	Preventive	4

PROJECTED SAVING

```
> def f(actual, predicted, cost):
    if actual==predicted:
        if actual=='Corrective':
            return 30000
        elif actual=='Preventive':
            return cost
        elif actual=='Healthy':
            return 0
    else:
        return cost
```

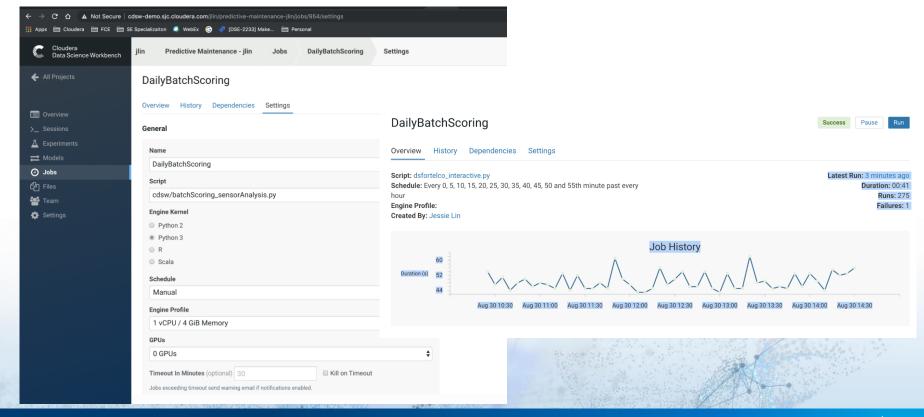
```
> csPD = costSavings.select('month',F.round('actualCost'),F.round('predictedCost')).toPandas()
> csPD.plot(kind='line', x='month')

<matplotlib.axes._subplots.AxesSubplot at 0x7f2aec200048>
```

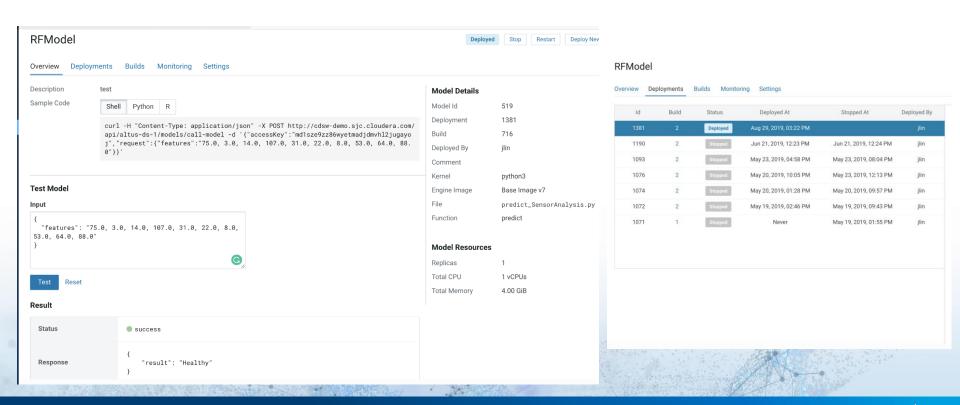




BATCH SCORING



DEPLOY THE MODEL AS A REST API





USE OF INTEL® AI TECHNOLOGY

HW: Intel[®] Xeon[®] Processors

SW: Intel[®] Math Kernel Library (Intel[®] MKL)

a. https://blog.cloudera.com/using-native-math-libraries-to-accelerate-spark-machine-learning-applications/





CONTACT

Jessie Lin Solution Engineer jlin@cloudera.com



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AI BUILDERS SHOWCASE ONECLICK.AI

AGENDA



- Company overview
- Business problem they solve by prioritized vertical
- Use of Intel® AI technology
- Results
- Contact



ONECLICK.AI



 OneClick.ai is an SaaS forecasting and optimization Platform powered by Automated Deep Learning(AutoDL) technology

 OneClick.ai empowers business analysts in Financial, CPG, Retail and Manufacturing industries to solve their top challenges in dayto-day operations with self-serving AI software to advance to AIdriven decision-making.

BUSINESS PROBLEM SOLVED



OneClick.ai can help enterprises to automate most of the customization of AI solutions



CHALLENGES



1. New territory of big data: new format, new sources

Temporal factors

Operational factors

Economical factors

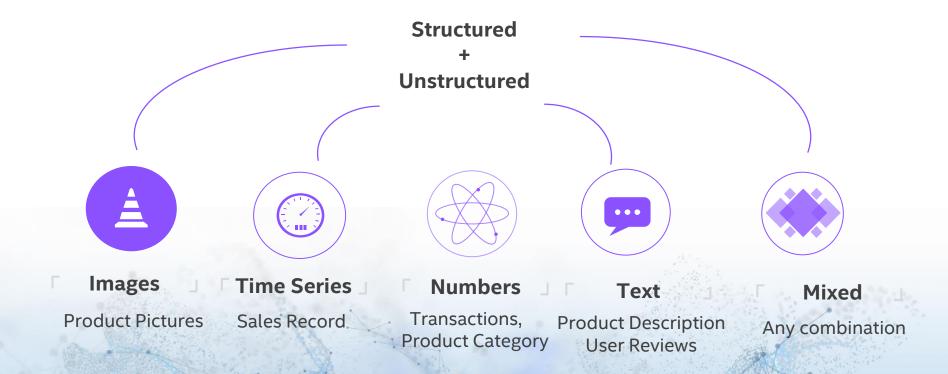
Product information

Social media

2. Traditional methods can not keep up what advanced Al technologies like Deep Learning can achieve

TECHNOLOGY





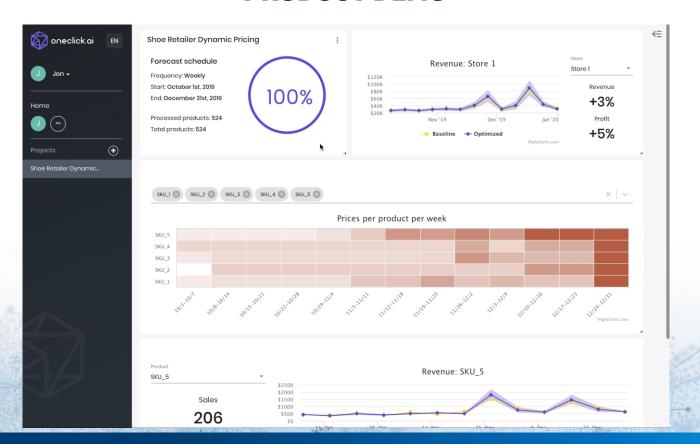
TECHNOLOGY





PRODUCT DEMO





USE OF INTEL® AI TECHNOLOGY



HW: Intel[®] Xeon[®] Scalable Processors

SW: Intel® Distribution for TensorFlow* 1.12 with Intel® MKL-DNN, Intel® Distribution for Python* 2.7

Data: Retail historical sales records with product attributes

Time Series Forecasting Model: Custom hybrid deep neural network models with near 450,000 trainable parameters

Improvement: Model training and inference time

CONTACT

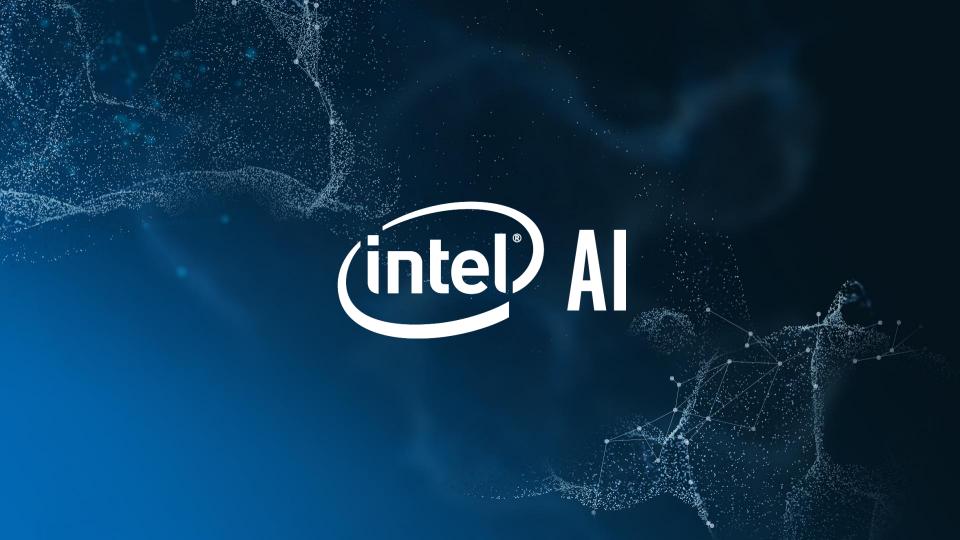


Yuan Shen Founder and CEO of OneClick.ai

yuans@oneclick.ai



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AI BUILDERS SHOWCASE QUEST GLOBAL INC.

AGENDA



- 1 Company Overview
- 2 QuEST Intel® AI Solutions
- 3 Use Of Intel® AI Technology
- 4 Financial Sector and Industrial Use case
- 4 Results
- 5 Contact

COMPANY OVERVIEW: QUEST GLOBAL INC.



Engineering-focused solutions provider to Fortune-500 Technology companies. We deliver next-gen technologies like AI/DL, IOT, AR/VR to accelerate digital transformation for the world's leading organizations.





- •Embedded & Software
 Product Engineering
- Enterprise Solutions
- •Technology Solutions: AI/DL, AR/VR, Big Data Analytics, IoT, Security

Engineering

(Mechanical Engineering, Design Engineering, Process and Manufacturing etc.)







65+ Locations



12,000+ Employees

QUEST-INTEL® AI SOLUTIONS



Healthcare & Medical Imaging

Retail, Banking, Hospitality & Education



Automotive & Transportation

Industrial & Manufacturing

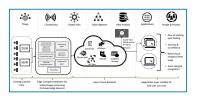
Intel® AI Builders

System Integration (SI) Partner

https://builders.intel.com/ai/membership/quest

QuEST AI Solutions in Intel® AI Builder Solution Catalogue (Intel® OpenVINO™/AI)

QuEST ThirdEye - Vision Analytics Platform



Intel AI Builders Solution Catalogue: https://builders.intel.com/ai/solutionscatalog/quest-thirdeye-vision-analytics-platform-554

QuEST Lung Nodule Detection (Early detection of Lung Cancer)

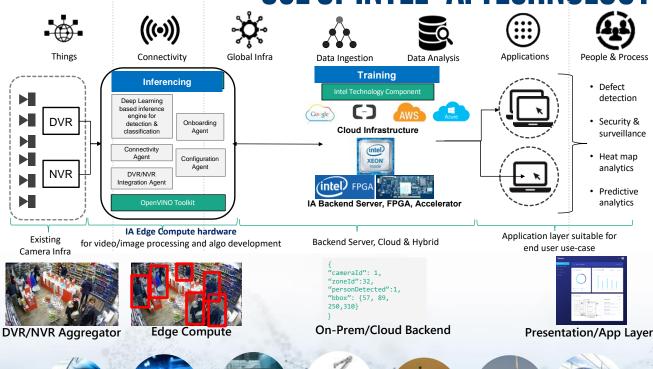


https://builders.intel.com/ai/solutionscatalo
Inte®l AI Builders Solution Catalogue: g/lung-nodule-detection-in-ct-scans-549



USE OF INTEL® AI TECHNOLOGY







Intel® Core™ i5/ i7 Intel® Xeon® (Skylake)

Intel® SW

Intel[®] Distribution of OpenVINO™ Toolkit

Showcasing vision application in Financial & Industrial verticals





Hi-Tech











Rail



RETAIL AND BANKING SECTOR APPLICATION Person Detection Targeted customer



Enable HVC

frictionless ATM

experience



Detect customers who may be having a bad experience in bank/retail space

· Emotion Detection · Age & Gender Recognition



Bank / Retail Reception Area

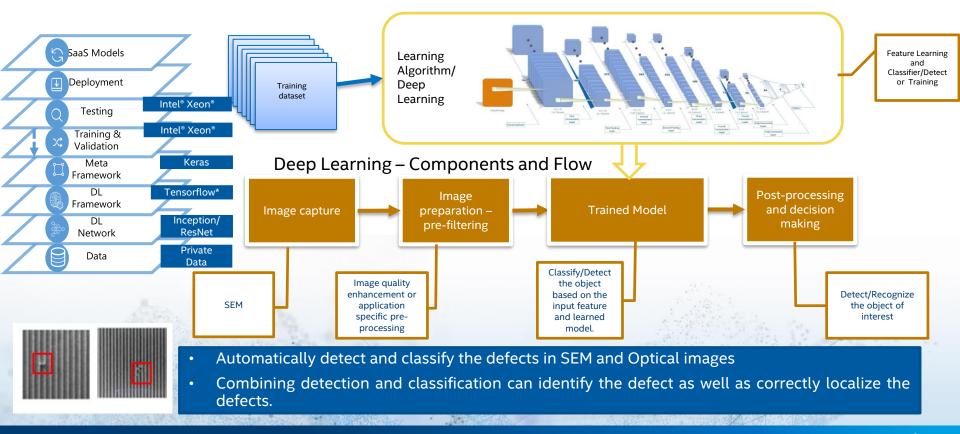
Predict customer

Recognize High **Value Customers** at the front of the teller line

Bank ATM / Kiosk **Premises**

INDUSTRIAL APPLICATION: AUTOMATIC DEFECT DETECTION & CLASSIFICATION

In semiconductor domain – to reduce overkills without compromising underkill reduction



RESULTS-INTEL TECHNOLOGY VALUE PROPOSITION FOR AI



Intel Technology Impact and value Proposition:

- Intel technology with Intel® Distribution of OpenVINO™ Toolkit gives the advantage of utilizing the existing CPU infrastructure to its fullest potential for creating accelerated AI inferencing applications.
- We are able to achieve this without compromising speed or quality.
- Customers are really interested in such solutions on edge devices.

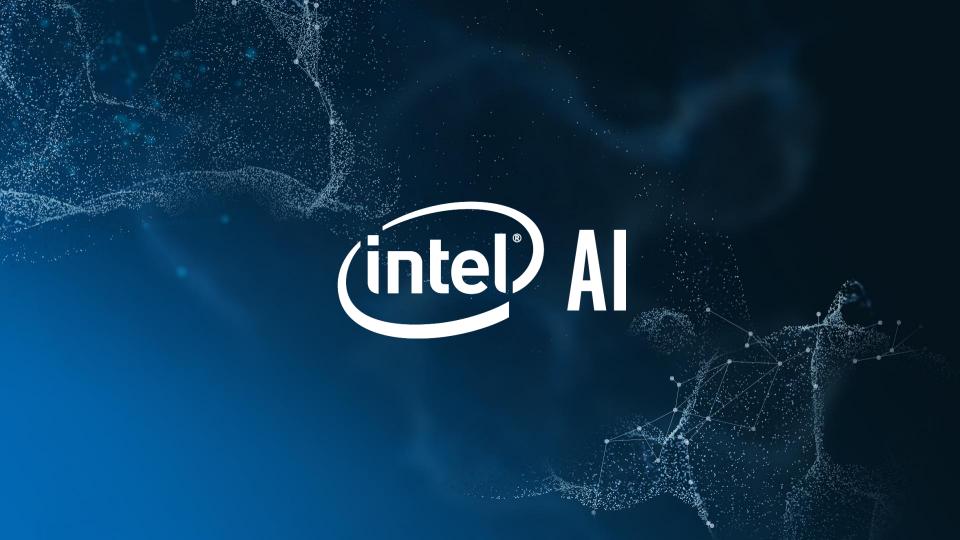
CONTACT



Dr. Rubayat Mahmud
Director of Sales and Business Development
Rubayat.Mahmud@QuEST-Global.com



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AI BUILDERS SHOWCASE GRAMENER INC

AGENDA

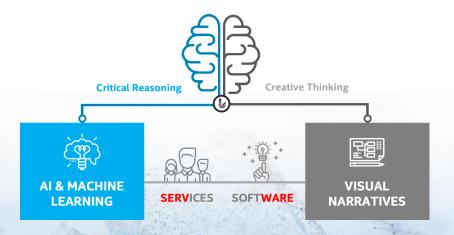
- Gramener overview
- Saving Antarctic Penguins with Deep Learning
- Approach and Intel[®] AI technology used
- Results
- Contact



Gramener

GRAMENER

- Solve Business Problems through consultative data science applications
- "Insights as Stories"



- 100+ clients, 200+ employees, 5 global locations
- Won Microsoft-CNBC award for Al



Gramener

SAVING ANTARCTIC PENGUINS WITH DEEP LEARNING



- Antarctica faces major environmental threats
- Penguin populations suffer the biggest impact



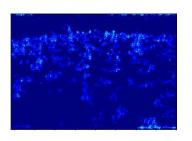
- Penguin watch is a citizen science initiative
- 100 cameras setup with 10+ years of time-lapse pictures



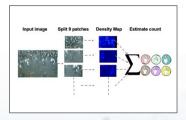
- Data labeling done by crowd-sourced annotations
- · Solution built by Gramener and Microsoft AI for Earth

Gramener

APPROACH & INTEL® AI TECHNOLOGY



- Challenges such as occlusion, density difference, perspective distortion and camera angles
- Crowd counting approach using density estimations



- Cascaded multi-column CNN architecture adopted
- High-level prior stage, followed by density estimation



- Intel[®] Xeon[®] processors
- Intel® Optimization for PyTorch*

RESULTS



- Robust solution with good accuracy (Mean Abs. Error < 10)
- Intel architecture benchmarked for high performance with cost savings



- Solution architecture repurposed for drug discovery
- Applied to estimate store footfalls & count crowds



- Al in Text analytics, Image recognition, Crowd counting
- Solutions such as CX VoC Analytics, Legal Contract Risk
- Data storytelling to solve specific business problems

CONTACT



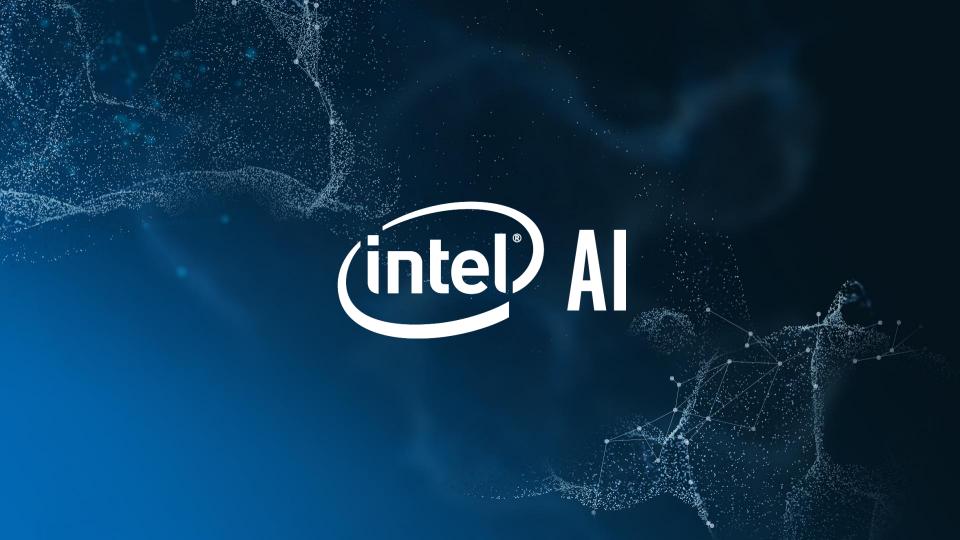
Naveen Gattu
Co-founder and COO

naveen.gattu@gramener.com



Ganes Kesari
Co-founder and Head of Analytics
ganes.kesari@gramener.com

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AI BUILDERS SHOWCASE WIPRO

AGENDA



- Company Overview
- Business Problem Solved
- Use of Intel® AI Technology
- Results



WIPRO OVERVIEW





IT Services Revenue in FY 2018-19

\$8.1Bn



Active global clients*

1060



Employee Count*

174,850



Countries with Employee presence*

57

Wipro's AI/ML Service Offerings



Al Consulting

Help in AI strategy
AI CoE- Operating Model
AI maturity assessment
AI Technology
Recommendations
AI Readiness
Business Case creation



Al Services

Productionizing POCs/Pilots
Deployment
Technology Identification
Build & Support AI Data
Platforms
Custom AI Models
Co-Create Solutions



ML Ops

Build, Maintain ML Data
Pipeline
Data Ingestion, Data
Exploration, Data Wrangling
Machine Learning Lifecycle
Mgmt
Al Validation as a Service



Al Apps

Industry Specific Solutions built on AI services like Vision, Speech, Video Intelligence, Chatbots, Language Understanding Custom Insights, Reports



INTEL® AI BUILDERS: BUSINESS PROBLEMS SOLVED



Pipe Sleuth



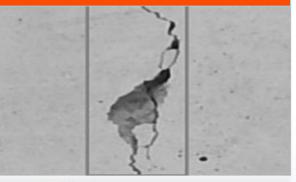
Pipeline Assessment

Medical Imaging



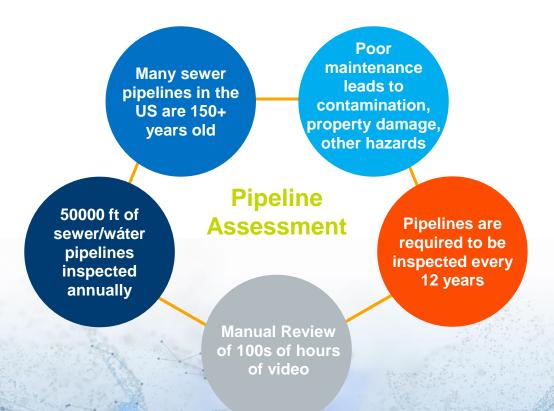
Lung Segmentation, Lung Disease Diagnosis

Surface Crack Detection



External Structural Cracks Inspection

PIPE SLEUTH - PROBLEM CONTEXT



PIPELINE VIDEO CAPTURE & ASSESSMENT PROCESS



Pipeline Video Capture





Pipeline Assessment

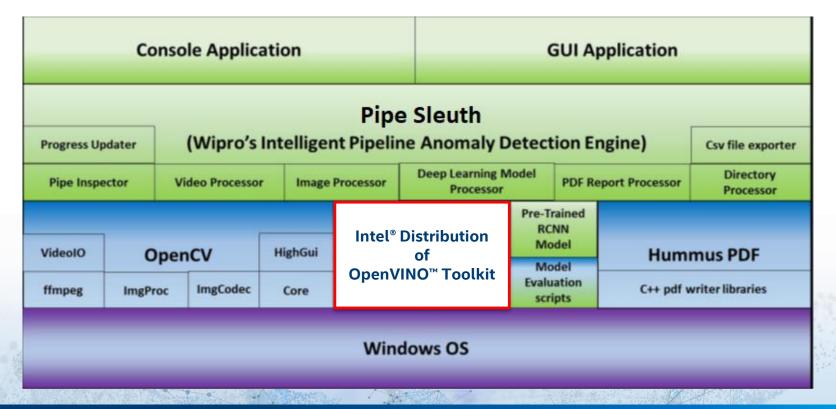




- Expert time: 1 hour -> 10-mins (for 1-hour of video)
- From Tedious -> Not Tedious
- Scalable, Reliable
 - ROI: 3.5-4x over 5 years

PIPE SLEUTH SOLUTION



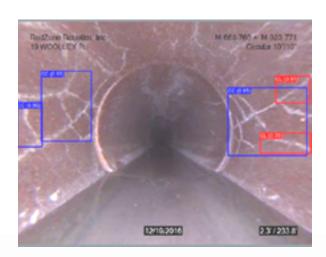


PIPELINE ANOMALIES









~20 Defect Types

- Cracks (Longitudinal, Circumferential, Hinge, Spiral, Multiple)
- Deposits (Attached Encrustation, Grease, Others; Settled – Others)

- Roots (Ball, Medium, Fine)
- Collapse
- Others

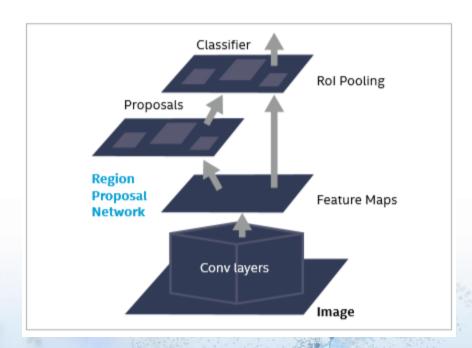
DEEP LEARNING MODEL



ResNet-101 (Feature Extractor)

layer name	output size	101-layer	
conv1	112×112	7×7, 64, stride 2	
conv2_x	56×56	3×3 max pool, stride 2 $\begin{bmatrix} 1 \times 1, 64 \\ 3 \times 3, 64 \\ 1 \times 1, 256 \end{bmatrix} \times 3$	
conv3_x	28×28	$\begin{bmatrix} 1 \times 1, 128 \\ 3 \times 3, 128 \\ 1 \times 1, 512 \end{bmatrix} \times 4$	
conv4_x	14×14	$\begin{bmatrix} 1 \times 1, 256 \\ 3 \times 3, 256 \\ 1 \times 1, 1024 \end{bmatrix} \times 23$	
conv5_x	7×7	$\begin{bmatrix} 1 \times 1, 512 \\ 3 \times 3, 512 \\ 1 \times 1, 2048 \end{bmatrix} \times 3$	
	1×1	average pool, 1000-d fc, softmax	
			. 103

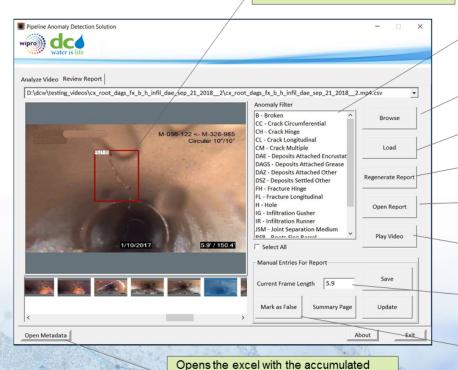
Faster R-CNN (Object Detector)



APPLICATION



Anomaly location and type are identified with confidence score



defect details for each video assessed

Lists all anomaly types found. User can select one or more types to review only those

User can select pre-evaluated video to review

Load anomaly images from selected video

Regenerate report with user updates

Open the original report and its folder location

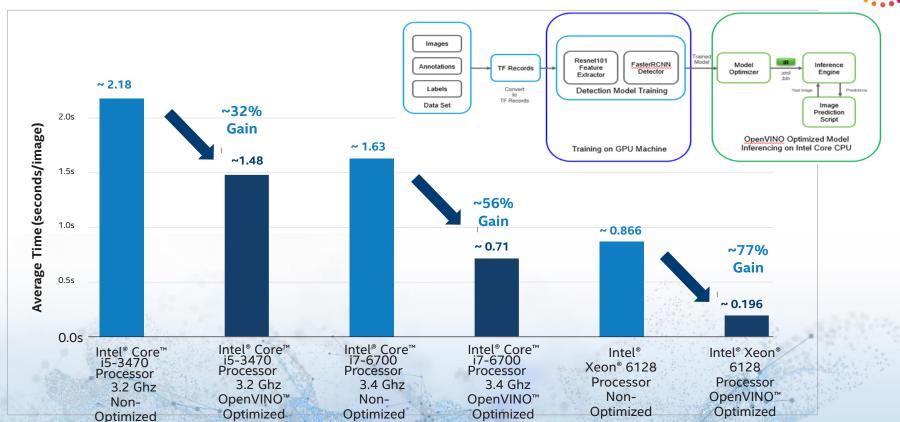
Launches video replay application

Extracted distance information from the frame. User can also update the same

User can mark individual frames as false and exclude from report

OPTIMIZATION ON INTEL® - PERFORMANCE





CONFIGURATION DETAILS

*Other names and brands may be claimed as the property of others.

Configuration: Intel® Xeon® Platinum 8153 CPU @ 2GHz with Intel® Distribution of OpenVINO™ toolkit. Testing done by Wipro Q3 2019 Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors.

Performance results are based on testing as of dates shown in configuration and may not reflect all publicly available security updates. No product can be absolutely secure. See configuration disclosure for details.

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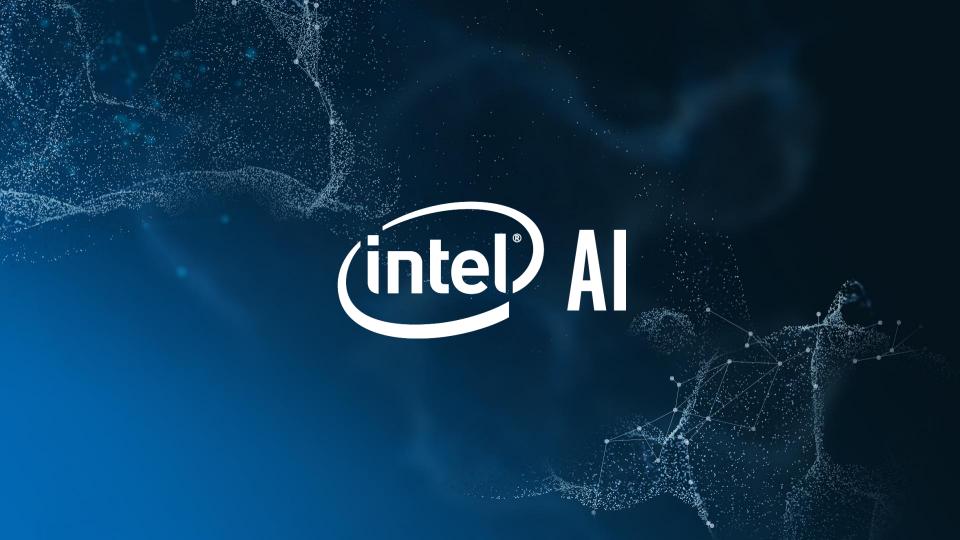
CONTACT





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Sunil Baliga
Director of Sales
sunil.baliga@wipro.com





INTEL

AI BUILDERS SHOWCASE INSTADEEP



AGENDA

- Company overview
- Business Problem
- Use of Intel® AI technology
- Results
- Contact





INSTADEEP

FOUNDED

2015

TEAM

R&D AI Engineers,
Software,
Hardware,
Visualization....

OFFICES

LONDON, TUNIS, PARIS, NAIROBI, LAGOS

WHAT WE DO

- Solve a wide range of prediction, classification and optimization problems for our global industrial partners
- Allow clients to unlock data insights, realize value, and increase efficiency and speed across their organization

OUR APPROACH

1. RESEARCH

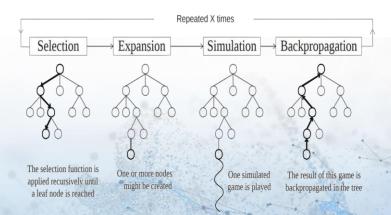
- 2. ENGINEERING
- 3. DEPLOYMENT, VISUALIZATION



BUSINESS PROBLEM SOLVED

Problem

Packing a set of items into fixed-size bins while minimising a cost function e.g. number of bins required, surface of the packing

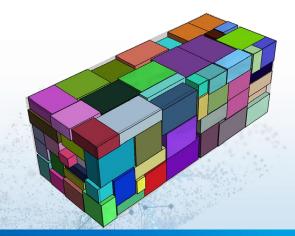


Methodaology

- Formulation as a Markov Decision Process
- AlphaZero-like Algorithm
 - Policy-Value Neural Network
 - Planning Algorithm
 - Adversarial Learning Process

Results

- Outperform a sophisticated heuristic, i.e. lego
- Approach applicable to many NP-Hard problems





USE OF INTEL® AI TECHNOLOGY

HW: Second Generation Intel® Xeon® Scalable Processors

Contrary to traditional deep learning, reinforcement learning relies heavily on general purpose computing both during training and inference. The workload can be highly parallelized and the training time is highly correlated to the number of cores used.





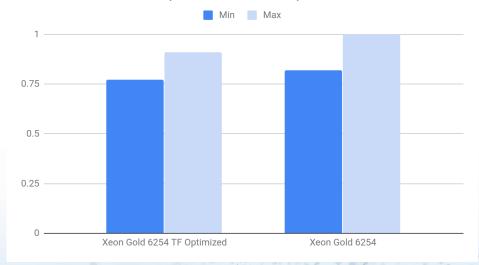
RESULTS

The benchmark consists in measuring the inference time which represents the major bottleneck in the **learning speed**.

We measured the average inference time from 10K inferences initiated from a Golang based agent dispatching the requests on a thread pool. Using 2nd Gen Intel® Xeon® Scalable Processors we see ~10% improvement in inference performance Intel® Optimization for TensorFlow*

Demo

Xeon Gold 6254 TF Optimized vs Non Optimized



> InstaDeep™

CONFIGURATION SPECIFICATIONS

*Other names and brands may be claimed as the property of others.

Configuration: Intel® Xeon® Gold 6254 @ 3.10GHz with Intel optimized TensorFlow. Testing done by InstaDeep August 2019

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors.

Performance results are based on testing as of dates shown in configuration and may not reflect all publicly available security updates. No product can be absolutely secure. See configuration disclosure for details.

Optimization Notice: Intel's compilers may or may not optimize to the same degree for non-Intel microprocessors for optimizations that are not unique to Intel microprocessors. These optimizations include SSE2, SSE3, and SSSE3 instruction sets and other optimizations. Intel does not guarantee the availability, functionality, or effectiveness of any optimization on microprocessors not manufactured by Intel. Microprocessor-dependent optimizations in this product are intended for use with Intel microprocessors. Certain optimizations not specific to Intel microarchitecture are reserved for Intel microprocessors. Please refer to the applicable product User and Reference Guides for more information regarding the specific instruction sets covered by this notice. Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit: http://www.intel.com/performance

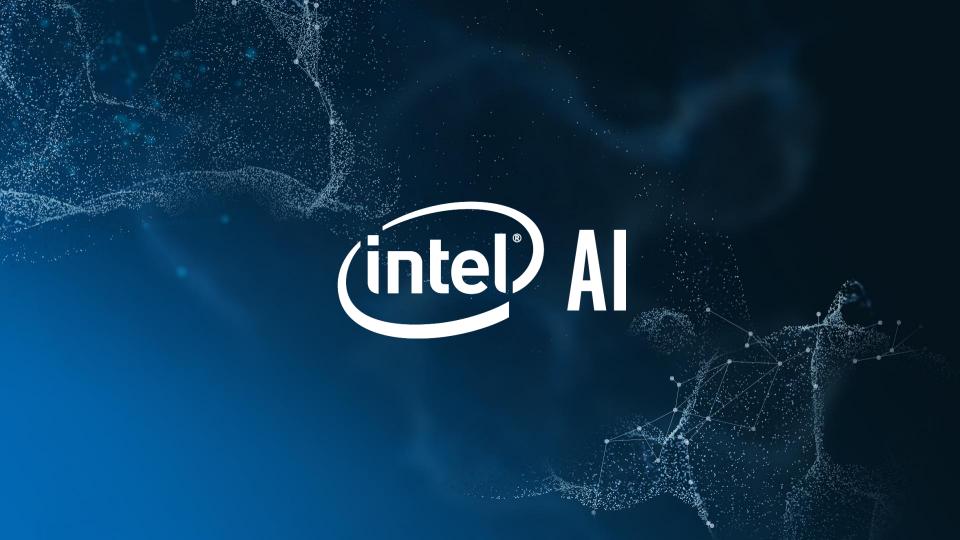


CONTACT

Amine Kerkeni Head Of Engineering ak@instadeep.com



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AI BUILDERS SHOWCASE JOHN SNOW LABS

AGENDA



- Accelerating AI in Healthcare
- Getting AI from concept to production in a high-compliance industry
- The Turnkey Enterprise Platform for Healthcare AI
- Optimized for Intel® AI Software & Hardware
- Customer Success
- Contact

ACCELERATING AI IN HEALTHCARE





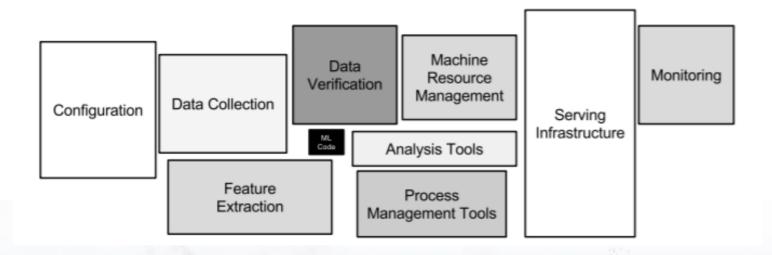






GETTING AI FROM CONCEPT TO PRODUCTION John Snow LABS

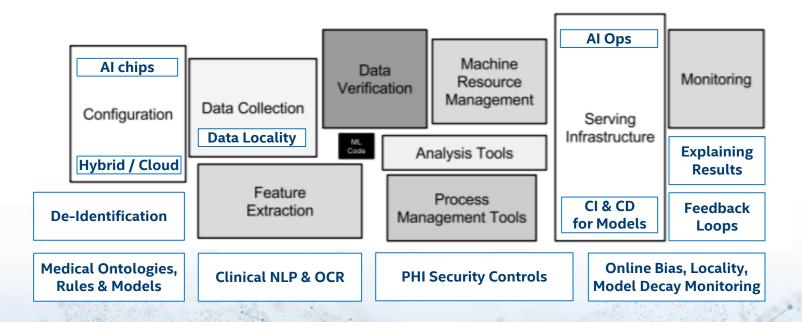




"Hidden Technical Debt in Machine Learning Systems", Google, NIPS 2015

GETTING AI FROM CONCEPT TO PRODUCTION





Building Real-World AI Systems in Healthcare & Life Science, 2019

THE TURNKEY ENTERPRISE PLATFORM FOR HEALTHCARE AI



Data Engineer



Data Analyst



App Developer

Data Integration

Self-serve data ingestion & wrangling

Data Exploration

Interactive data analysis without coding

Data Science

Interactive notebooks in Python & R

Model Deployment

Scalable & Secure model API's

Dataflows

Batch & streaming data ingestion

Connectors

Input & output to standard protocols

Curated Medical Data

Current, Cleaned & Enriched

Data Quality

Monitor feed health in real time

Governance

Search metadata, lineage & stats

Search

Full-text, faceted & geospatial search

Visualization

Real-time, drag & drop dashboards

Time Series

Interactive time series analysis

Metadata

Dataset & schema search

SOL

Live SQL editor & dashboards

Notebooks

JupyterLab and JupyterHub

Machine Learning

Certified ML & data mining libraries

Deep Learning

Train & debug on CPU's and GPU's

Natural Language

Spark NLP with TensorFlow

Vision & OCR

Pre-trained deep learning models

Model Server

Deploy models as micro-services

CI & CD for Models

Auto-test & deploy machine learning

Model Repository

Reusable & versioned model store

Pipelines

Track experiments, jobs, and runs

API Gateway

Security, logging & rate limiting

Hub

Portal & Single Sign on

Orchestration

Hardened Kubernetes

Operations



Monitoring

Pre-configured agents & alerts

Identity

Authentication, Authorization, Audit

USE OF INTEL® AI TECHNOLOGY

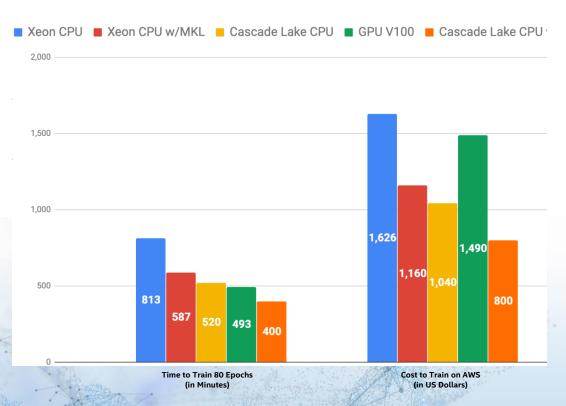


Goal: Get the most from your Intel hardware, out-of-the-box

Software: Intel® MKL, MKL-DL, Intel® Optimization for TensorFlow*

Hardware: 2nd Gen Intel® Xeon® Scalable Processors

Results: Training Deep-Learning NLP on Intel® Xeon® Scalable Processors 2nd gen is 19% faster and 46% cheaper than GPU-optimized. Training on Tesla v100. Tested on AWS.



CONFIGURATION SPECIFICATIONS

*Other names and brands may be claimed as the property of others.

Configuration: Intel® Xeon® Platinum 8175M @ 2.5GHz with Intel optimized TensorFlow*. Testing done by John Snow Labs September 2019

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors.

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CUSTOMER SUCCESS





Improving Patient Flow Forecasting at Kaiser Permanente



Usermind built its data science platform from scratch to production in 3 months



How Roche Automates Knowledge Extraction from Pathology Reports



Using Advances Analytics to Improve
Mental Health for HIV-Positive Adolescents



How SelectData Uses AI to Better Understand Home Health Patients



Feature Engineering with Spark NLP to Accelerate Clinical Trial Recruiting

CONTACT

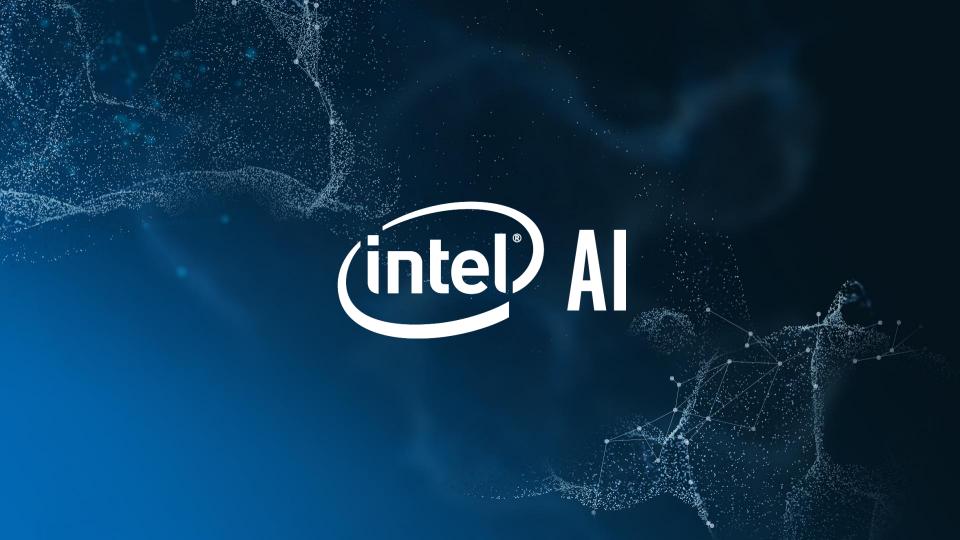


Dr. David Talby

david@johnsnowlabs.com



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AI BUILDERS SHOWCASE SKYMIND

AGENDA

- Skymind overview
- Business problem solved by prioritized vertical
- Use of Intel® AI technology
- Results
- Contact



►skymind

SKYMIND



Enabling the AI-Driven Enterprise

Accelerating Experimentation to Production Lifecycle

Founded 2014

Funding \$17.5M Funding, Series A Startup

Open Source DeepLearning4J Suite > 200k downloads/mo **DL4J

Enterprise Skymind Intelligence Layer (SKIL) AI Infrastructure

Services Professional Services, Customer POCs

Investors





















BUSINESS PROBLEM SOLVED

Vision

Our mission is to bridge the gap between Deep Learning Workflows and delivering scalable production-ready AI Deployment



Many of our large enterprise clients engage to advance their machine intelligence, accelerate & scale their operating cycle initiatives, delivering value faster along their <u>AI Maturity Journey</u>.

Business Challenges

Many State-of-the-Art (SOTA) models require high memory, distributed compute power, and a proper strategy to minimize latency to gain insights from their data.

Modeling Algorithms

Infrastructure Compute Device Performance Al Platforms Vendor Partnerships

















servicenow

relative sampling of select Industries



Computer Vision Image Classification High Dimensionality



NLP Text

Agents and Bots High Cardinality & Context

USE OF INTEL® AI TECHNOLOGY

Intel Hardware Resource Management Skymind Intel OSS State of the Art (SOTA) DL4J DL4J Model Architectures Model Zoo **Model Import** JVM + Pvthon **Polyglot Support** Java/Scala Managed JVM DeepLearning4J (DL4J) Suite Native C/C++ libND4J **JavaCPP** Intel BLAS Intel® MKL-DNN **OSS Native Framework Model Training** Model Inference Infrastructure Infrastructure **HW Infrastructure Layer** intel (intel) - COLFAX Optimized Multi-Core **XEON** XEON' **CPU Architecture**

Native JVM Model Zoo VGG16 CNN Architecture 2-Class 4600+ Image Dataset, Batch Size=128

DL4J Suite Beta Release 1.0.0-BETA4 CONV Layers/Operations support Intel® Math Kernel Library (Intel® MKL)

Adaptive backend switch across chip architectures

MKL (Math Kernel Library) Performance Benchmarking Native DL4J Models on Intel® MKL-DNN

2nd Generation Intel® Xeon® Scalable processors 1 x 6248 (Intel® Xeon® Gold) Multi-Core CPU @2.5 GHz 2 x 8256 (Intel® Xeon® Platinum) Multi-Core CPU @3.8 GHz

RESULTS

Our partnership with Intel® has enabled Intel® MKL-DNN as the default execution on 2nd Gen Intel® Xeon® Scalable processors, replacing OpenBLAS* per optimized Linear Algebra

Facilitating Faster Time to Market, Productivity, and Operational Costs

6X 42 min Training Time Possibly Reduced Operational Cost

5.9X 6 records/sec Throughput (Record) Leading to Reduced Inference Latency Time

Reference the following for upcoming published DL4J Benchmarks software.intel.com/en-us/frameworks github.com/IntelAI/models/benchmarks

CONFIGURATION SPECIFICATIONS

Skymind DL4J Benchmark on Custom Code using VGG16 - Throughput Performance on Intel® Xeon® Platinum 8256 Processor:

NEW: Tested by Intel as of 09/03/2019. 2 socket Intel® Xeon® Platinum 8256 Processor, 4 cores per socket, Ucode 0x500001c, HT On, Turbo On, OS Ubuntu 18.04.2 LTS, Kernel 4.15.0-48-generic, Total Memory 384 GB (12 slots/ 32GB/ 2666 MTs), Deep Learning Framework: DL4J (from Intel_benchmark branch of https://github.com/deeplearning4j/dl4j-benchmark), custom train data of 2 classes with a total 4608 samples from Distracted Driver Dataset from kaggle with batch size of 128 for VGG16. Trained with MKLDNN & KMP_BLOCKTIME as zero & KMP_AFFINITY as fine, balanced

BASELINE: Tested by Intel as of 09/03/2019. 2 socket Intel® Xeon® Platinum 8256 Processor, 4 cores per socket, Ucode 0x500001c, HT On, Turbo On, OS Ubuntu 18.04.2 LTS, Kernel 4.15.0-48-generic, Total Memory 384 GB (12 slots/ 32GB/ 2666 MTs), Deep Learning Framework: DL4J (from Intel_benchmark branch of https://github.com/deeplearning4j/dl4j-benchmark), custom train data of 2 classes with a total 4608 samples from Distracted Driver Dataset from kaggle with batch size of 128 for VGG16. Trained without MKLDNN with maxbyte as 376GB, maxphysicalbytes as 373GB and Xmx as 6GB

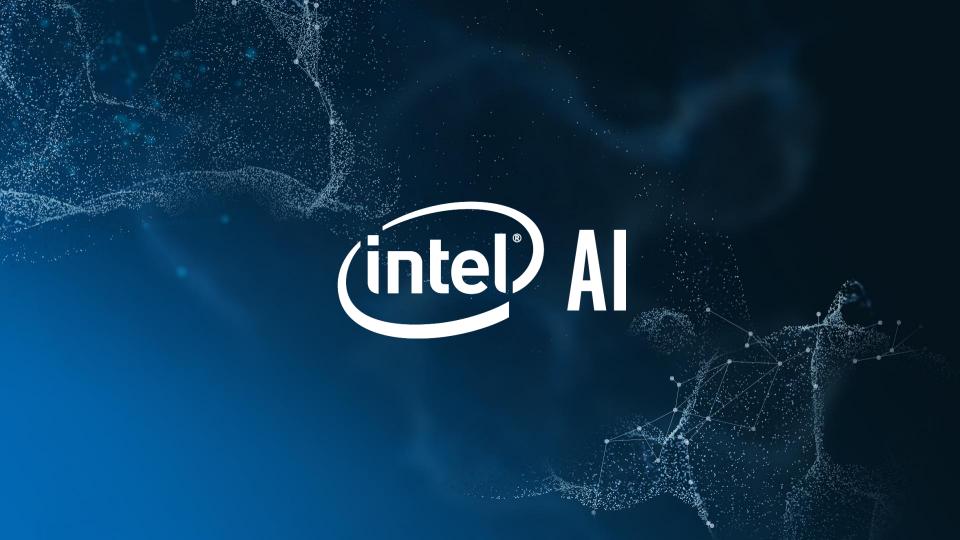
CONTACT

Ari Kamlani
Principal Deep Learning Technologist
ari@skymind.io



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Learn more: builders.intel.com/ai/membership/skymind





AI BUILDERS SHOWCASE DIGITATE



AGENDA



- Digitate overview
- Business problem solved by prioritized vertical
- Use of Intel® AI technology
- Results
- Contact





Launched in June 2015

100+ Customers globally

Managing millions of Applications, Batch, SAP, & Infra components

600+ Employees worldwide

70+ Patents globally





Best Overall AI Company of the Year



Tata Steel for Operational Efficiency





Software Defined Infrastructure

: digitate

The Business

Operational

Excellence

Awards 2019

Finalists: Loblaw & Credit Suisse

Transformation &



Most Innovative Tech

Company of the Year

Software Company of the Year



Fastest Growing Software Company of the Year



BUSINESS PROBLEMS SOLVED



- Digital transformation for an enterprise
 - Powering IT operations with ignio, AI/ML based Cognitive Platform
 - Data-driven context aware insights, predictions and actions
 - Superior experience with integration of context-rich collaboration channels & varied types of data

Challenges

- Scale: 100K+ technology components
- Complexity: Deep and dynamic interdependencies, diversity of technology vendors & versions
- Accelerating rate of change: Technology footprint, everything on-demand,
 Expectations: SLAs → instant gratification, cyber security threats and regulatory requirements

UP TO 5.8X INCREASE

in Inference workload performance over baseline using Intel® Distribution of OpenVINO™ toolkit





Challenge: Reduce image processing/interpreting time for face detection, in a deep learning workload for workflow automation. Additionally, there was a need to adhere to UX design guidelines for facial login response time.

: digitate

Solution: The solution took advantage of efficient multicore processing on Intel Xeon® Scalable processors along with Facenet* and witnessed significant improvement in Inference performance, thereby bringing down the time to detect the face for auto-login. This resulted in quicker login times by performing image inferencing in milliseconds now (vs. seconds earlier), while adhering to UX requirements.

Partner: Digitate is a pioneer in bringing artificial intelligence to IT and Business Automation. Digitate 's Al-enabled software, ignio™, helps organizations run IT infrastructures far more efficiently, improves customer experience and increases the agility and stability of IT operations

Intel® OpenVINO™ improves FaceNet inference times by up to 1051X, resulting in an end-to-end 5.8X improvement in Digitate's autologin process, with Intel® Xeon® Platinum 8256 processors.

*Other names and brands may be claimed as the property of others.
Configuration: Intel® Xeon® Platinum 8256CPU @ 3.80GHz/4 cores per socket / 2 sockets / 384GB / Tensorflow* 1.13.1/ OpenVINO ™ 2019 R1.1.133

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors.

Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results

You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit http://www.intel.com/performance. Performance results are based on testing as of August 2018 and may not reflect all publicly available security updates. See configuration disclosure for details. No product can be absolutely secure.

Whitepaper/Solution **Brief: WIP**





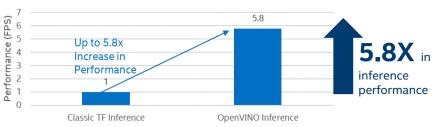
RESULTS



- Enhanced Cognitive Automation Performance with Intel® technology
 - Use of Intel® Distribution of OpenVINO™ toolkit delivered significant, tangible performance improvements compared to Tensorflow
 - Both the deep learning-based face recognition workload (FaceNet) and ignio's overall autologin workload benefited from Intel hardware and software

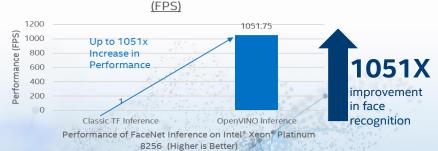
ignio, along with Intel® AI hardware and software, delivers the capabilities and performance required for future-ready, agile and proactive IT operations and infrastructure.

Relative Digitate Autologin Performance (FPS)



Performance of Full Autologin Application on Intel® Xeon® Platinum 8256 (Higher is Better)

Relative FaceNet Inference Performance





USE OF INTEL® AI TECHNOLOGY





Hardware: 2nd Generation Intel[®] Xeon[®] Scalable processor

Choosing the right hardware helps drive IT efficiency, agility and gain a competitive edge

An infrastructure to seamlessly scale and support demanding AIOps workloads

Robust & reliable platforms for the data-fueled enterprise

OpenVINO

Software: Intel® Distribution of OpenVINO™ toolkit

Incredible efficiency and hardware acceleration achieved through Intel® Distribution of OpenVINO™ toolkit

The toolkit supports heterogeneous execution across various hardware through a common API

Includes easy to use libraries and preoptimized kernels

CONFIGURATION SPECIFICATIONS



Digitate Custom Face Detector:

NEW: Tested by Intel as of 05/19/2019. 2 socket Intel® Xeon® Platinum 8256 Processor, 4 cores per socket, HT On, Turbo On, Total Memory 374 GB (12 slots/ 32GB/ 2666 MHz), BIOS: SE5C620.86B.02.01.0008.031920191559, Deep Learning Framework: Intel® OpenVINO® 1.1.133 using Intel® MKL 2019.3. FaceNet topology customized by Digitate*, custom test data, tested using batches of 1 (optimized for latency).

BASELINE: Tested by Intel as of 05/19/2019. 2 socket Intel® Xeon® Platinum 8256 Processor, 4 cores per socket, HT On, Turbo On, Total Memory 374 GB (12 slots/ 32GB/ 2666 MHz), BIOS: SE5C620.86B.02.01.0008.031920191559, Deep Learning Framework: TensorFlow 1.13.1 (Anaconda repo 'tensorflow') using Intel® MKL 2019.3. FaceNet topology customized by Digitate*, custom test data, tested using batches of 1 (optimized for latency).



: digitate

Name: Anjali Gajendragadkar

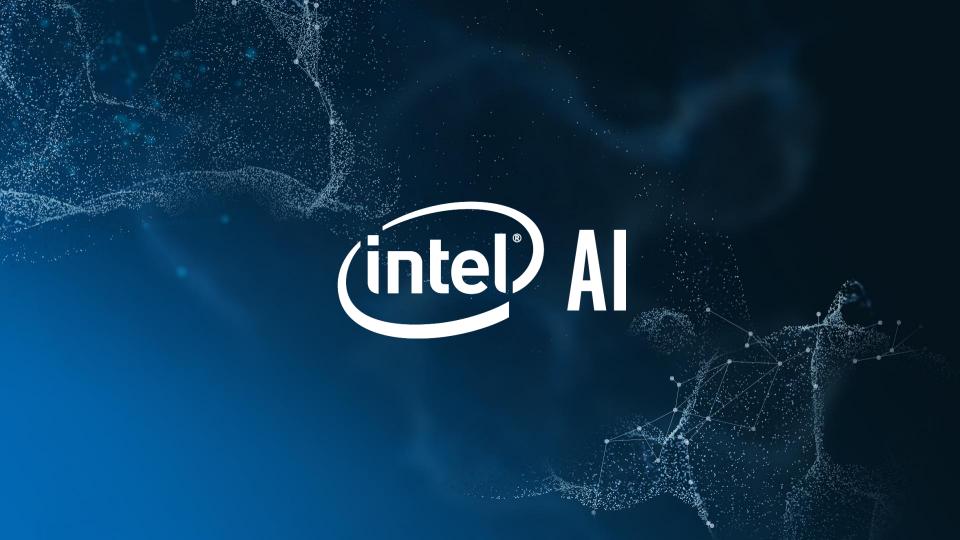
Title: ignio™ Product Manager

Email address: anjali.sg@digitate.com



Visit our table with your questions and to see a demo, or stop by the Intel® AI Builders matchmaking table to set up a private meeting.

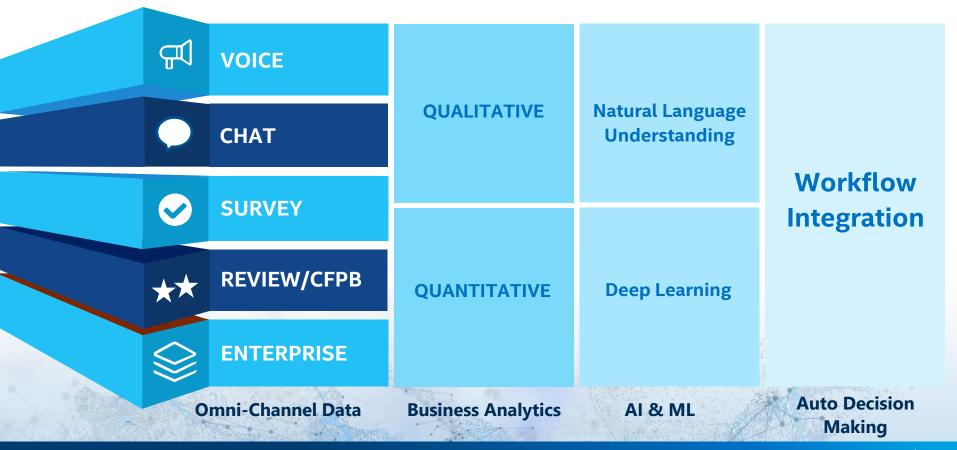
We look forward to meeting you!





AI BUILDERS SHOWCASE STRATIFYD

END-TO-END CUSTOMER ANALYTICS PLATFORM POWERED BY AI



USE OF INTEL AI TECHNOLOGY





ARTIFICIAL INTELLIGENCE

Al Solutions Catalog (Public & Internal)























Compliance

Digital Transformation

Voice of Employee

Partner Micro-Ecosystem

DEEP LEARNING DEPLOYMENT

Intel® Distribution of OpenVINO™ Toolkit

Intel® Movidius™ SDK

Open Visual Inference & Neural Network
Optimization toolkit for inference
deployment on CPU/GPU/FPGA for TF,
Caffe* & MXNet*

Optimized inference deployment on Intel VPUs for TensorFlow* & Caffe*

AI FOUNDATION



Workflow Integration Layer

Visualization & Reporting Layer

Al & Machine Learning Layer

OMNI-CHANNEL Data Layer

RESULT IN STRATIFYD AI MARKETPLACE





- Voice of the Customer
- Voice of the Employee
- App Store Feedback
- Complaint vs. Feedback
- Compliance
- Revenue
- Operational Efficiencies
- Many more...

mastercard. Intuit. Microsoft

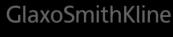












charles SCHWAB













Mercedes-Benz







LIVEPERSON



CONTACT

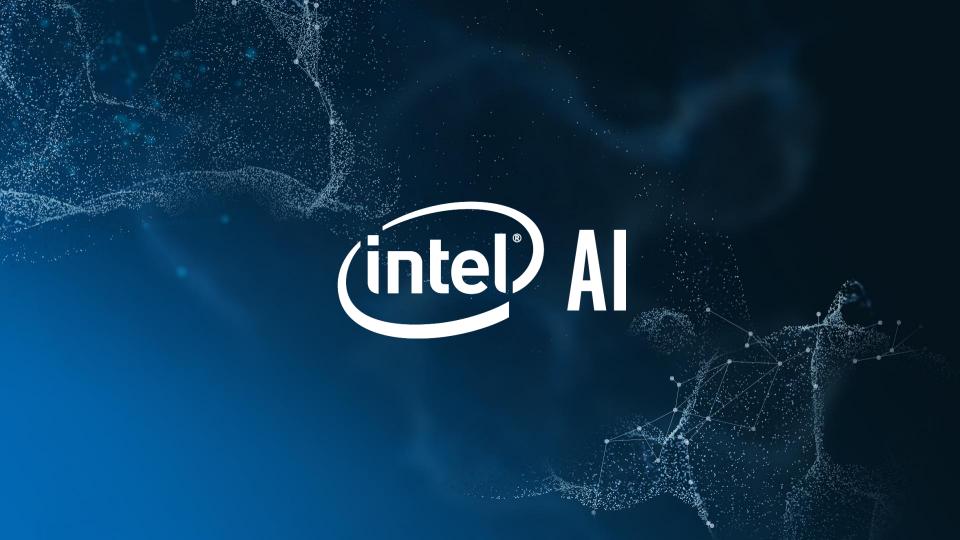


Derek Wang Founder and CEO

Derek.Wang@stratifyd.com



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AI BUILDERS SHOWCASE CISCO

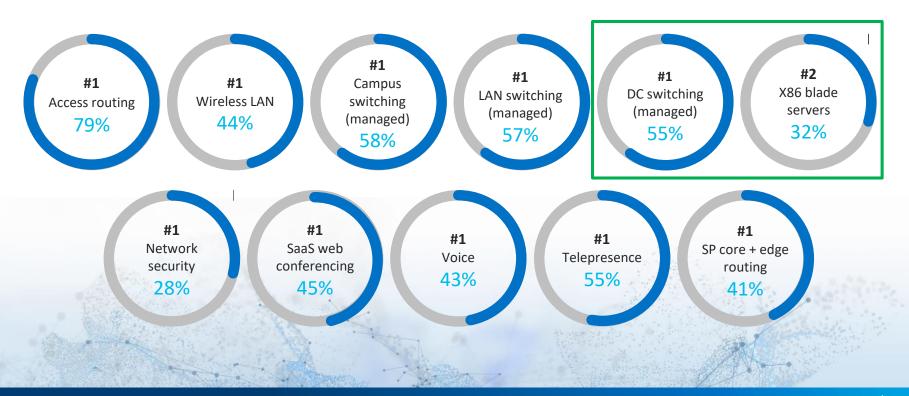
AGENDA



- Cisco Overview
- How Cisco is creating a bridge on AI solutions
- Retail Shelfsight Solution
- Use of Intel® AI technology
- Contact

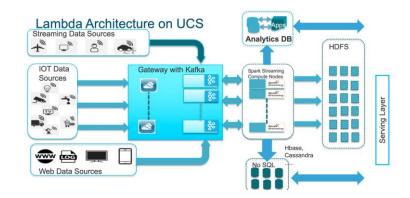


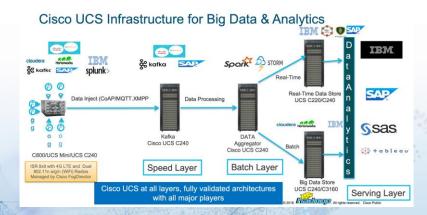
CISCO SYSTEMS OVERVIEW



BUSINESS PROBLEM SOLVED

- Gap between Data Scientists and IT
 - Data Scientists: Data Pipeline
 - IT: Infrastructure
- Retail: Remote Locations
- Need to Accelerate Deployment



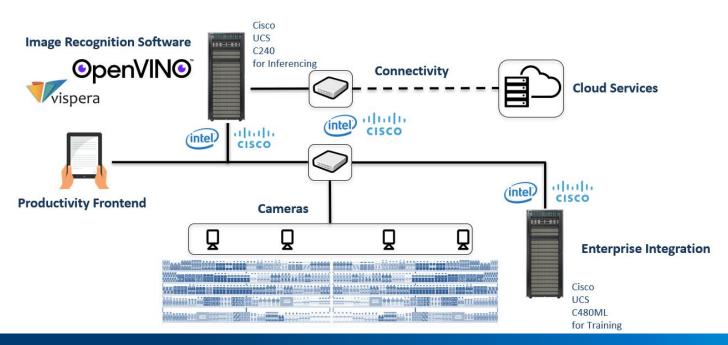


USE OF INTEL® AI TECHNOLOGY



HW: Cisco UCS C240 M5 servers using Intel® Xeon® Gold 6230 Processor

SW: Intel® Distribution of OpenVINO™ Toolkit



RESULTS



In this solution we are running ML Inferencing in a Cisco USC system with Intel® Xeon® Scalable processors – without any GPU or accelerator card.

Intel® Distribution of OpenVINO™ Toolkit is integrated on Vispera solution and configured for Intel® Xeon® Scalable processors optimization.



Product Recognition

Real-Time Out-of-Stock Detection



Shopper Tracking



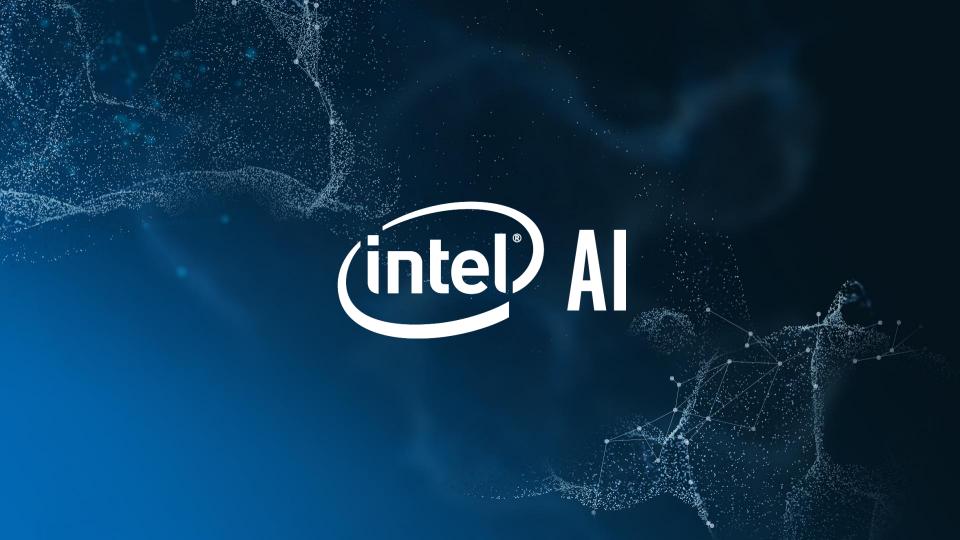
CONTACT



Han Yang, PhD
Senior Product Manager – Cisco Datacenter Group
hanyang@cisco.com



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INTE

AI BUILDERS SHOWCASE HEWLETT PACKARD ENTERPRISE



HEWLETT PACKARD ENTERPRISE (HPE)



Accelerating enterprise AI innovation with software, services, and infrastructure

Speed the design and deployment of your Al strategy Expertise and advisory services to accelerate your journey with **HPE Pointnext**

Give your data science teams instant access to AI tools and data Industry's only turnkey, container-based software platform purpose-built for AI: BlueData

Build your AI models Execute your strategy at scale with less fast, cost-effectively, with less risk complexity Most comprehensive Pay-per-use consumption Al infrastructure solutions, models that deliver cost, control and agility with from edge to cloud, optimized for Intel architecture **HPE GreenLake**

THE CHALLENGE: ML OPERATIONALIZATION



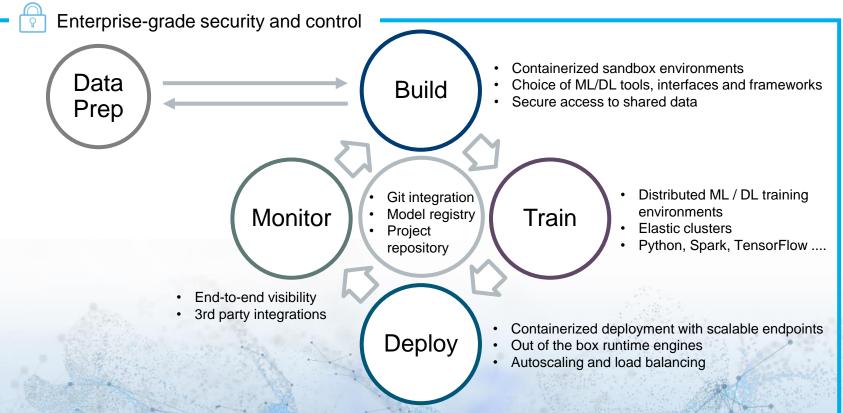
As AI and ML initiatives mature, the biggest challenge faced by technical professionals is operationalizing ML for effective management and delivery of models. By 2021, at least 50% of machine learning projects will not be fully deployed. Operationalization of machine learning is an imperative step in aligning AI investments with strategic business objectives — the "last mile" toward delivering business value.

Source: Gartner, A Guidance Framework for Operationalizing Machine Learning for AI, October 24 2018.

Only operational ML pipelines deliver business value

OPERATIONALIZATION FOR THE ML LIFECYCLE

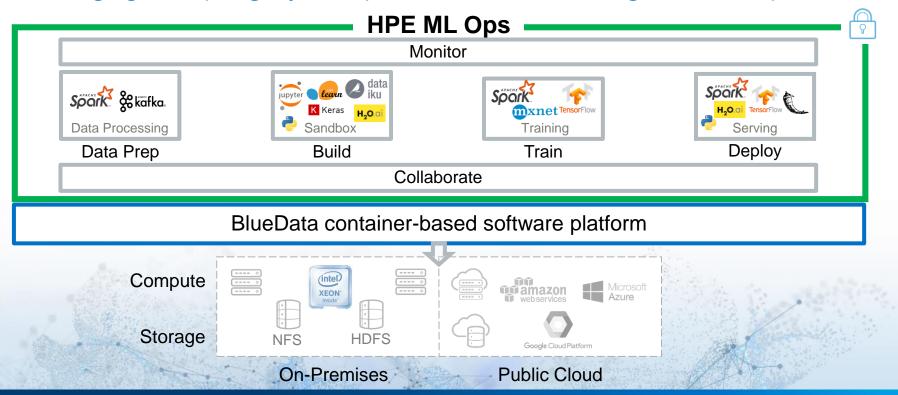




NEW: HPE MACHINE LEARNING OPS (ML OPS)



Bringing DevOps agility and speed to machine learning in the enterprise



HPE ML OPS - KEY BENEFITS



Faster Choice Rapid deployment Ease of use Time-to-Value of tools **Improved** Collaboration Reproducibility High performance **Productivity** Reduced Data and model Security and High availability control Risk governance **Flexibility** On-prem, cloud, Multi-tenancy Scalable clusters or hybrid and Elasticity

COMPREHENSIVE AI INFRASTRUCTURE SOLUTIONS



Starter

Enterprise Platform for Al

Petaflop Scaling for Deep Learning

High Memory Edge Inference Edge Inference & Analytics

Powered by 2nd Generation Intel® Xeon® Scalable Processors









SGI 8600



Superdome Flex



Apollo 2000



Edgeline 4000

Example Workload / Use Case

- Proof of Concept
- Small Training
 Workloads
- Prescriptive Maintenance
- FraudDetection

- Leading Edge Simulations
- High Performing
 Al Applications
- In-Memory
 Analytics
- Large Compute Environments
- Smart Cities
- Speech-to-Text
- Image-Based Recognition
- Autonomous Driving

Training Focus

Scale

Inference Focus

Edge-Intensive

CONTACT





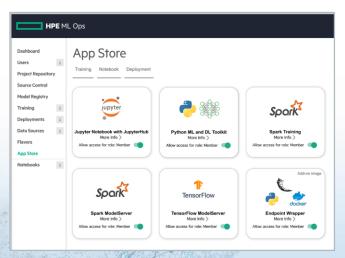
Nanda Vijaydev
Data Scientist and Distinguished Technologist
Hewlett Packard Enterprise (BlueData)

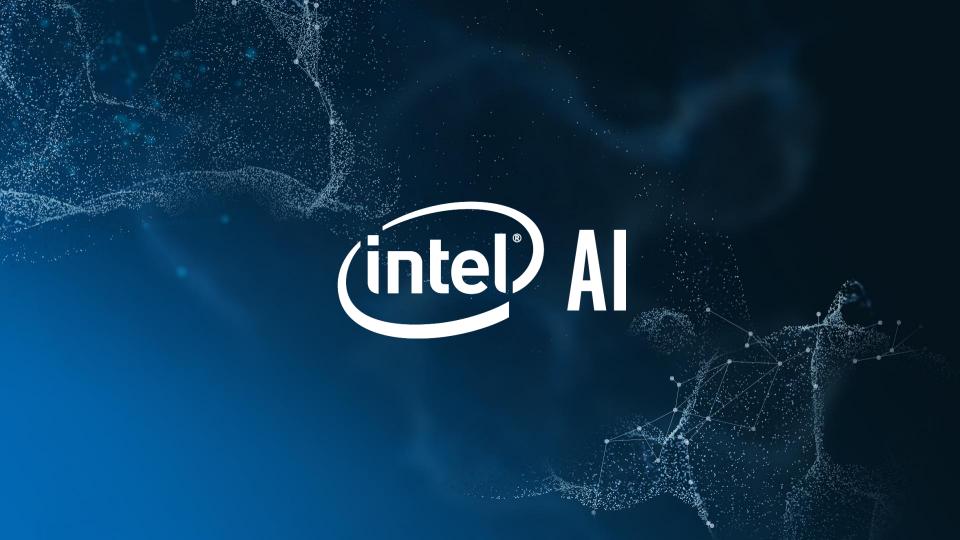
nanda.vijaydev@hpe.com

Visit our table with your questions, and see a demo in our booth at the O'Reilly AI Conference this week

Learn more about HPE AI solutions at https://example.com/AI

Learn more about HPE ML Ops at hpe.com/info/MLOps





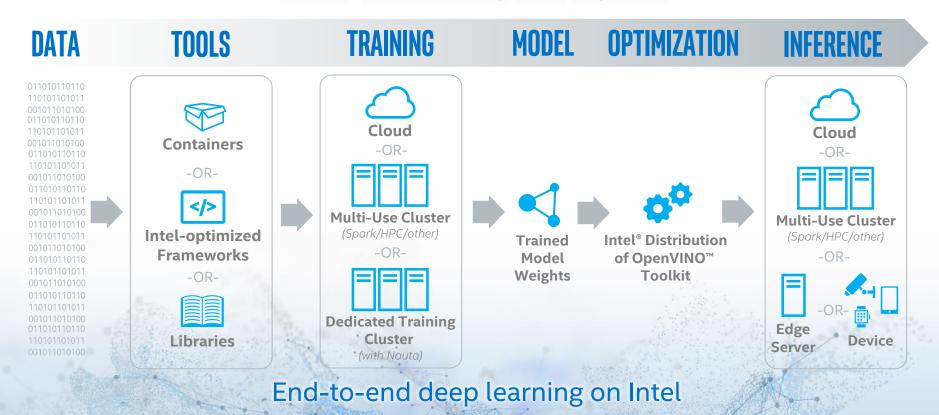
HOW TO DEPLOY ON INTEL® ARCHITECTURE

RAVI PANCHUMARTHY

INTRO

- Intel collaborated with major CSP's and OEM partners to include pre-configured optimized DL environments, allowing data scientists and deep learning practitioners to instantly have access to Intel Optimized DL environments.
- This portion of the showcase will allow you to learn about Intel Optimized AI environments for your workloads, be it in the cloud or in your data center.
- Starting w/ CSPs
 - Microsoft Azure
 - Google Cloud Platform (GCP)
 - Amazon Web Services (AWS)
- Then with OEMs
 - HPE
 - Inspur
 - Lenovo
 - Dell

DEEP LEARNING DEPLOYED







HARDWARE

- Azure offers Intel[®] Xeon[®] Processor family
 - The fastest VMs on Azure
 - Intel® Xeon® Platinum 8168 processor, features an all-cores clock speed greater than 3 GHz for most workloads.
 - Intel® AVX-512 instructions will provide up to a 2X performance boost to vector processing workloads.
 - Compute-Optimized VMs with up to 72 vCPUs, 144 GBs of memory.
 - HC-series VMs expose 44 non-HT CPU cores and 352 GB of RAM, featuring 100 Gb/s InfiniBand from Mellanox
 - Intel® Xeon® Scalable processors.
 Instances: FSv2, HC

SOFTWARE

- Intel Data Science VM (DSVM)
 - Intel optimized deep learning frameworks preconfigured and ready to use.
 - Deploy: http://aka.ms/dsvm/intel
- Azure Machine Learning service*
 - Cloud service that you use to train, deploy, automate, and manage machine learning models, all at the broad scale that the cloud provides.
- Get started with \$200 in free credits.



- Product Page: http://aka.ms/dsvm/intel
- Intel blog: https://www.intel.ai/intel-optimized-data-science-virtual-machine-azure/
- Microsoft blog: https://azure.microsoft.com/en-us/blog/intel-and-microsoft-bring-optimizations-to-deep-learning-on-azure/



HARDWARE

- GCP offers latest Intel® Xeon® Processor family
 - 40% performance improvement compared to current GCP VMs**
 - Built-in Acceleration with Intel® Deep Learning Boost
 - Compute-Optimized VMs with up to 60 vCPUs, 240 GBs of memory, and up to 3TB of local storage**
 - Memory-Optimized VMs with up up to 12 TB of memory and 416 vCPUs**
 - Learn more: https://cloud.google.com/intel/
 - 2nd Gen Intel® Xeon® Scalable processors: C2, M2

SOFTWARE

Deep Learning VM

- Intel optimized deep learning frameworks preconfigured and ready to use.
- Deploy: <u>https://console.cloud.google.com/marketplace/details/click-to-deploy-images/deeplearning</u>

Al Platform

- build ML applications with a managed Jupyter Notebook service that provides fully configured environments for different ML frameworks using Deep Learning VM Image
- Get started with \$300 in free credits. https://cloud.google.com/free/

^{**}https://cloud.google.com/blog/products/compute/introducing-compute-and-memory-optimized-vms-for-google-compute-engine



- Product Page: https://console.cloud.google.com/marketplace/details/click-to-deploy-images/deeplearning
- Intel blog: https://www.intel.ai/google-cloud-platform/
- Community blog: https://blog.kovalevskyi.com/deeplearning-images-revision-m9-intel-optimized-images-273164612e93





COMMON HISTORY & VALUES

JOINT PRIORITIES

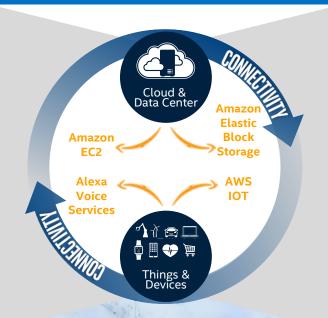
10+ year partnership

Joint development

Shared customer passion

High performance + low costs

World class supply chain



AI/ML/Analytics

HPC

Digital Transformation

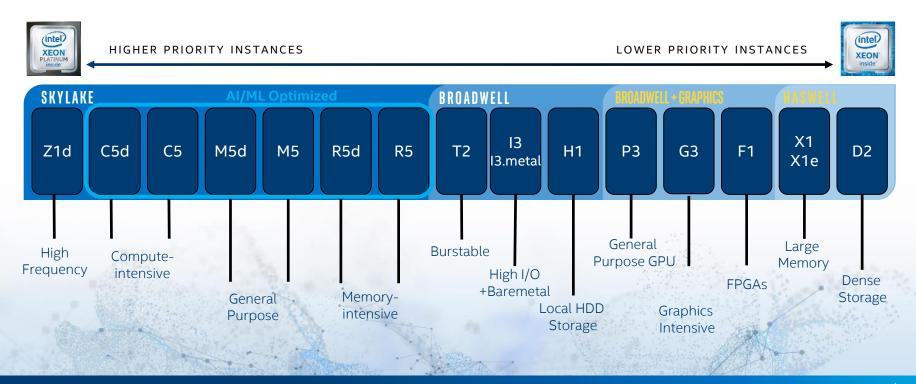
Cloud/Hybrid

Edge Computing

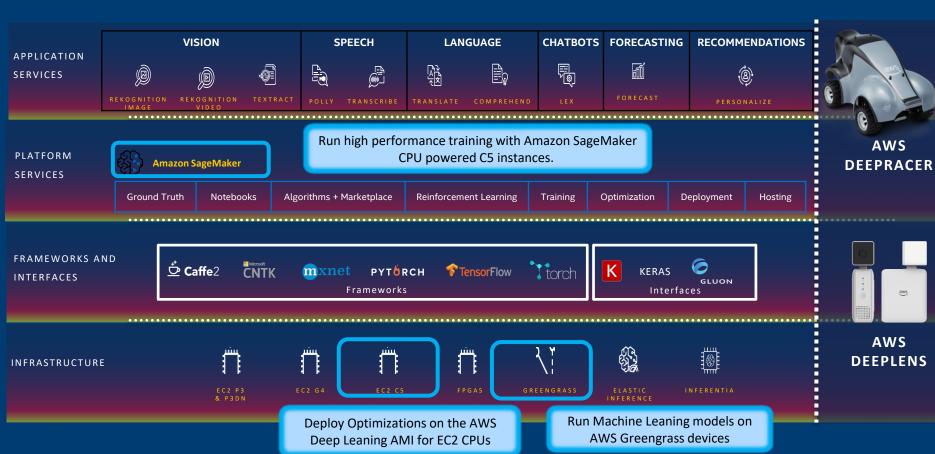


AWS EC2 INSTANCE SUMMARY

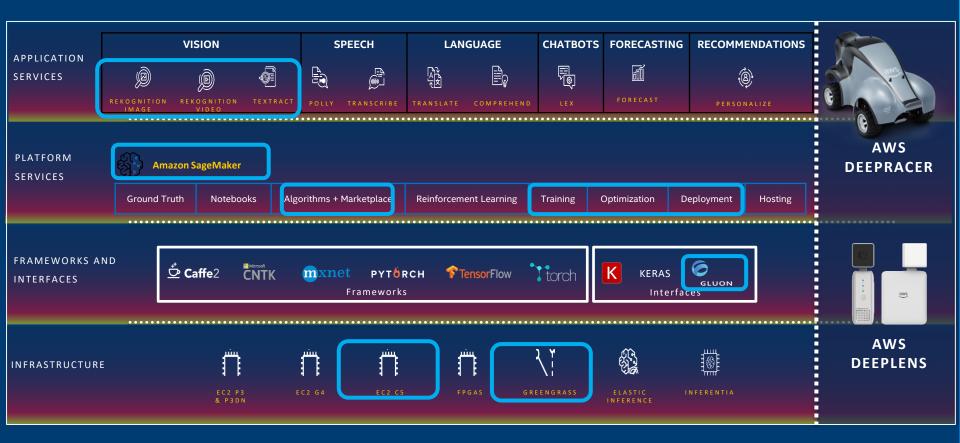
EC2 Instance Details: https://aws.amazon.com/ec2/



AWS MACHINE LEARNING STACK



AWS MACHINE LEARNING STACK



USE CASES

[Hyper]Personalization – Product Recommendations, Advertising, Cross/Upselling, Next Best Action, Nudges, Livestock Management



Time-series analysis – Forecasting, Anomaly Detection, Preventive Maintenance, Fleet Balancing

databricks

Computer Vision – Visual Inspection, Quality Assurance, Document-driven Workflows

THORN 1

Natural Language Processing - CX transformation, Market Intelligence, Translation, Sentiment Analysis



Cortexica Interiors Localisation Local Photo ID (Singapore) Cortexica BodyParts Localiser Mighty Anonymize Deep Vision brand recognition API Face blocking or blurring for Privacy Logo Recognition in Images Face Anonymizer Cortexica Interiors Localisation Skin Disease Classification Image collage classifier Passport Data Page Detection Deep Vision visual search API Waste Classifier **Barcode Detection** Deep Vision brand recognition API Vehicle Attribute Detection Deep Vision vehicle recognition Cortexica BodyParts Localiser Image mosaic classifier Image human classifier Image text classifier

MACHINE LEARNING PARTNER ACCELERATION PROGRAM

Accelerating customer POC's on latest generation of Aloptimized instances: C5, R5, M5, C5d, R5d, M5d

Partners accelerate customer adoption of Machine Learning optimized AWS instances

- Targeting customer opportunities for ML workloads running on C5/M5/R5 instance families
- Funding available is Intel cash + AWS credits (cash will be matched up to same value of provided AWS credits)
- Targeting a 10X ROI in AWS ARR on the joint Intel & AWS investment.

Each APN Competency Partner has access to potential funding for up to 5 POCs @ \$20k cash + \$20k credits

KEY TAKEAWAYS

AWS/Intel collaboration and programs make it easy to innovate

Intel instance types and Atom accelerate the development of AI/ML solutions

AWS Marketplace facilitates GTM motions and reaching new customers

How to engage

- Reach out to the AWS Intel Account Team to request support for the partner investment program: aws-inteloppty@amazon.com
- 2. We will look at the business case to evaluate our joint investment, typically starting with an AWS simple monthly calculator to show the services and instances that will be consumed.
- 3. Success stories can then be used at AWS re:Invent, Summits, AWSome Days, and social media

Key Contacts



Peter Bevan Amazon Account Manager WW **Gareth Tucker** Amazon Account Manager EMEA **Kapil Bansal** Amazon Account Manager APAC Marisa Reid Amazon Sales Development AI/ML Programs



Uday TennetyGlobal Strategic Alliances Manager

Celia Baker WW Intel Alliances PM

CONTACT US: aws-intel-oppty@amazon.com



- Product page: https://aws.amazon.com/machine-learning/amis/
- Intel blog: https://www.intel.ai/amazon-web-services-works-with-intel-to-enable-optimized-deep-learning-frameworks-on-amazon-ec2-cpu-instances/
- AWS blog: https://aws.amazon.com/about-aws/whats-new/2018/11/tensorflow1_12_mms10_launch_deep_learning_ami/





HEWLETT PACKARD ENTERPRISE (HPE)



Accelerating enterprise AI innovation with software, services, and infrastructure





COMPREHENSIVE AI INFRASTRUCTURE SOLUTIONS



Starter

Enterprise Platform for Al

Petaflop Scaling for Deep Learning

High Memory Edge Inference Edge Inference & Analytics

Powered by Second Generation Scalable Intel Xeon Processors









SGI 8600



Superdome Flex



Apollo 2000



Edgeline 4000

Example Workload / Use Case

- Proof of Concept
- Small Training
 Workloads
- Prescriptive Maintenance
- FraudDetection

- Leading Edge
 Simulations
- High Performing AI Applications
- In-Memory Analytics
- Large Compute Environments
- Smart Cities
- Speech-to-Text
- Image-Based Recognition
- Autonomous Driving

Training Focus

Scale

Inference Focus

Edge-Intensive



AI BUILDERS SHOWCASE INSPUR

AGENDA



- Company overview
- Business problem Solved
- Use of Intel® AI technology
- Performance Results
- Contact



INSPUR





Inspur is one of the world's leading hardware innovators and solutions providers for AI and deep learning. We are developing smarter, more powerful end-to-end solutions that help businesses leverage and tackle intelligent technologies from cloud to edge.



Global Top 3 Provider

One of the world's 3 largest server and solutions vendors



Serving Major Global CSPs

Fulfills 90% Al server demand with T1 CSP in China



Select Partner for FPGA

Chosen Al partner by major global CSPs for FPGA projects





Inspur Full-Stack Al Capabilities

SOFTWARE STACK

FRAMEWORK

Caffe-MPI • TensorFlow • Caffe • CNTK • MXNET

Inspur AlStation • Inspur Teye

INFERENCE





BUSINESS PROBLEM SOLVED



Cloud FPGA





Inspur F10A Arria10 Based, HHHL, 45W

Inspur F10S Stratix10 Based, FHHL, 150W

FPGA-as-a-Service









NF5260M5 Al Edge Server

Edge Computing







Image Recognition



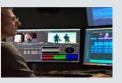
Autonomous Driving







Financial Terminals



Video Editing



CAD/Content Creation

OpenVINO

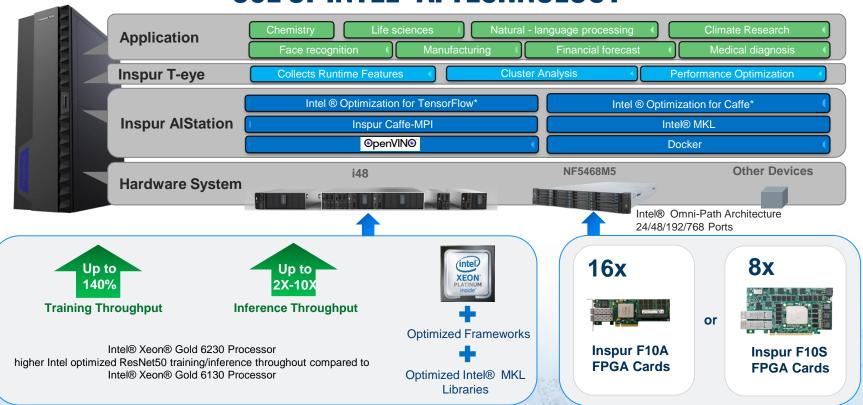
Based on convolutional neural networks (CNN), the toolkit extends workloads across Intel® hardware (including accelerators) and maximizes performance.

Inpsur TF2 FPGA Tool Kit

A powerful open-sourced complement to OpenVINO[™] by model cropping and model compression for supporting the CNN, Transformer, LSTM which can be quickly ported on FPGA

USE OF INTEL® AI TECHNOLOGY

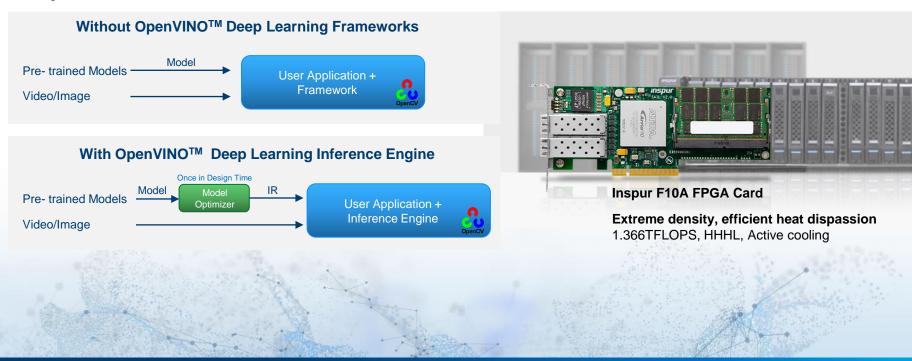




Deliver significant AI performance with hardware and software optimizations on Intel® Xeon® Cascade Lake processor

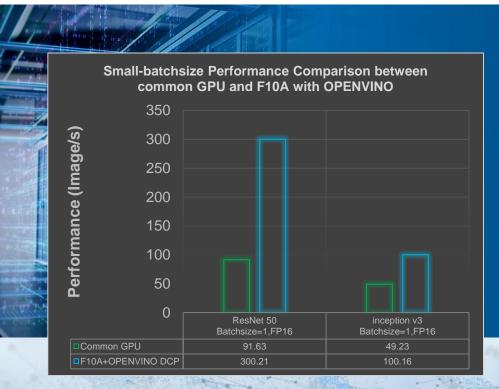
USE OF INTEL® AI TECHNOLOGY

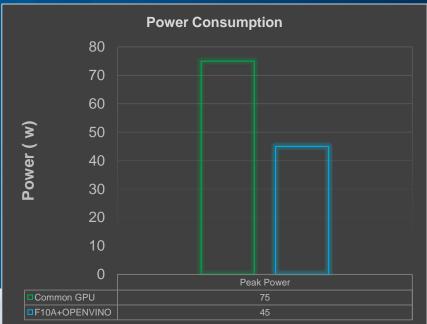
OpenVINO



RESULTS







CONFIGURATION SPECIFICATIONS

Tested by Inspur on 06/15/2019 by using NF5280M5, Inspur server with 2-socket Intel Xeon 6140 Processor, total Memory 374 GB (12 slots/ 32GB/ 2666 MHz). Deep Learning Framework: OpenVINO 2019R1 on F10A, Tensorflow 1.12 on GPU, tested using Batchsize of 1.



CONTACT

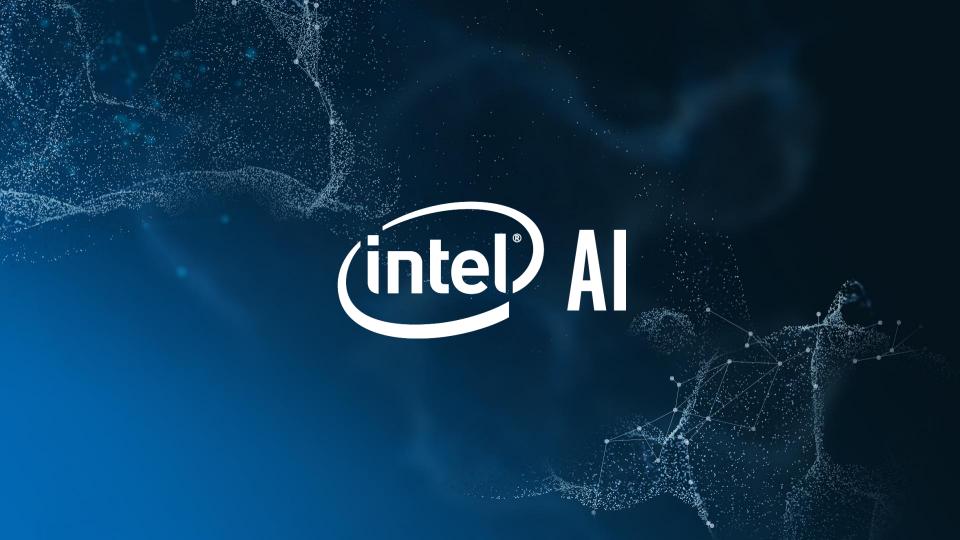


Bob Anderson VP of Sales

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AI BUILDERS SHOWCASE LENOVO

AGENDA



- Company overview
- Business problem they solve by prioritized vertical
- Use of Intel® AI technology
- Results
- Contact



LENOVO



Lenovo Data Center Group - an experienced and innovative global team

1st #1 1st Provder of Warm water cooled In x86 Server Hyperconverged supercomputers on Intel Scalable Xeon Reliability revenue growth** the TOP500 list & 6 years running*** Supercomputer fastest growing HPC company * 10,000 48.6% 20M+ Growth rate Service professionals Servers shipped Owned/controlled manufacturing sites Fastest growing server vendor * https://bit.ly/2N537Eu

** https://lnv.gy/2yjPYSv
***ITIC 2016/2017, ITIC 2017/2018, ITIC 2018/2019

Lenovo

BUSINESS PROBLEM SOLVED

- Architecting, deploying, and optimizing infrastructure for AI is HARD
 - Usage patterns unpredictable HOW MUCH INFRASTRUCTURE?
- Al clusters are most efficient for IT, but least understood by Data Scientists
 - System setup takes much effort WASTED DATA SCIENTIST TIME
- Lenovo Intelligent Computing Orchestration (LiCO) software solves both problems
 - LiCO + Intel-based clusters enable infrastructure optimization
 - GUI-based solution for HPC&AI makes deploying AI workloads fast & simple
 - Data Scientists can run tuning jobs in parallel, increasing their productivity

Lenovo

USE OF INTEL® AI TECHNOLOGY

HW: Full Lenovo server portfolio featuring Intel® Xeon® Scalable processors

- From a single 2U/2S to the largest warm water cooled supercomputers
- Flexibility to deploy whatever infrastructure fits your datacenter best for AI

SW: Leverages the latest Intel® AI framework optimizations

- Containerized framework deployment eliminates setup time for users
- Maximum performance on Intel® Xeon® Scalable processors for every job deployed



RESULTS



With LiCO + Intel-based solutions:

- Data Science users get more productive
 - Software stack setup + job deployment takes seconds
 - No need to learn cluster tools to take advantage of the powerful resources
 - Run multiple jobs in parallel to tune hyperparameters faster
- IT gains more infrastructure efficiency too
 - Al Infrastructure can start small and easily grow, or Al can converge with HPC
 - More user productivity drives higher utilization

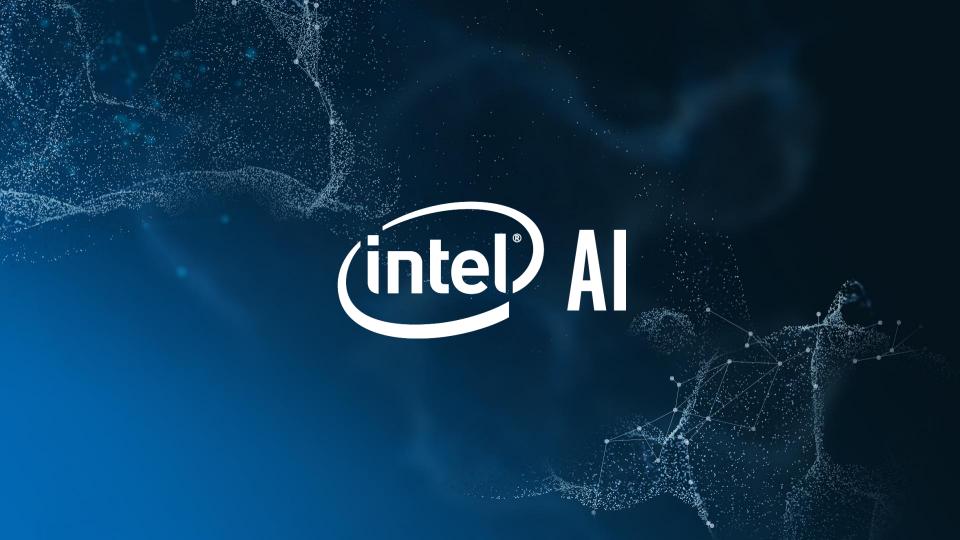


CONTACT

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AI BUILDERS SHOWCASE DELL EMC

AGENDA



- Company overview
- Business problem they solve by prioritized vertical
- Use of Intel® AI technology
- Results
- Contact



DELL EMC COMPANY OVERVIEW



Optimal configurations of workstations, servers, storage, networking, data center options and software



POWEREDGE SERVERS

designed for HPC environments, including more local I/O capacity and expandability



NETWORKING

delivering the bandwidth and speed you need



PRECISION WORKSTATIONS

for high-demand, industry specific applications



ACCELERATORS

offering the horsepower needed for bigger simulations faster than ever before





STORAGE

powerful performance, density and efficiency for data-intensive HPC environments





MANAGEMENT

deploy customers over bare metal with single pane of glass management



SERVICES

providing end-to-end services to maximize HPC investments



CLOUD

build or scale, rent cycles, and/or get burst capacity

READY SOLUTIONS FOR AI WITH INTEL

A full package of all the components needed

Hardware

Faster deployment

from months to weeks

Dell EMC
PowerEdge servers
storage • networking

Intel Accelerators

Libraries and frameworks

Optimized

for fast application development

Easy to use

for modeling

BigDL • TensorFlow

- Horovod
- MLKDNN

Software

Ready-to-go

data management and data science tools

Cloudera • Hortonworks
Spark • Kubernetes
Nauta

Services

Drive rapid adoption

and optimization of new environment

Strategy • Integration
Data engineering
Upskilling of resources
Ongoing support

DEEP LEARNING WITH INTEL® - VALUE

Primary target

 Customers looking to leverage Kubernetes/Docker environments for running deep learning workloads

Differentiation

- New Dell EMC IP: Kubernetes-based use case templates
- Nauta to simplifies model development and orchestrate of arduous tasks
- Validated infrastructure stack providing the power and scalability to build and train machine and deep learning models

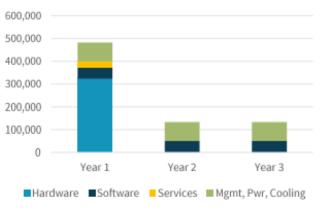
DEEP LEARNING WITH INTEL® - PACKAGE

- 1. Nauta Enterprise
- 2. R740xd login/master node
 - Intel® Xeon® Scalable Gold processors
 - 384GB RAM
 - 144TB (12x 12TB 112TB usable)
- 3. C6420 compute nodes
 - Intel® Xeon® Scalable Gold processors
 - 192GB RAM
 - No user-accessible storage
- 4. 10Gb Dell Networking S4048-ON
- 5. Isilon H600 NAS

RESULTS

Deep Learning with Intel

Deep Learning Training Three-year TCO 24 Hr Compute, 100TB Storage



Leading Public Cloud AI Service

Deep Learning Training Three-year TCO 12 Hr Compute, 10TB Storage





Source: ESG Technical Validation, Dell EMC Ready Solutions for AI: Deep Learning with Intel, April 2019. This InstaGraphic highlights results from an ESG Technical Validation commissioned by:



CONTACT

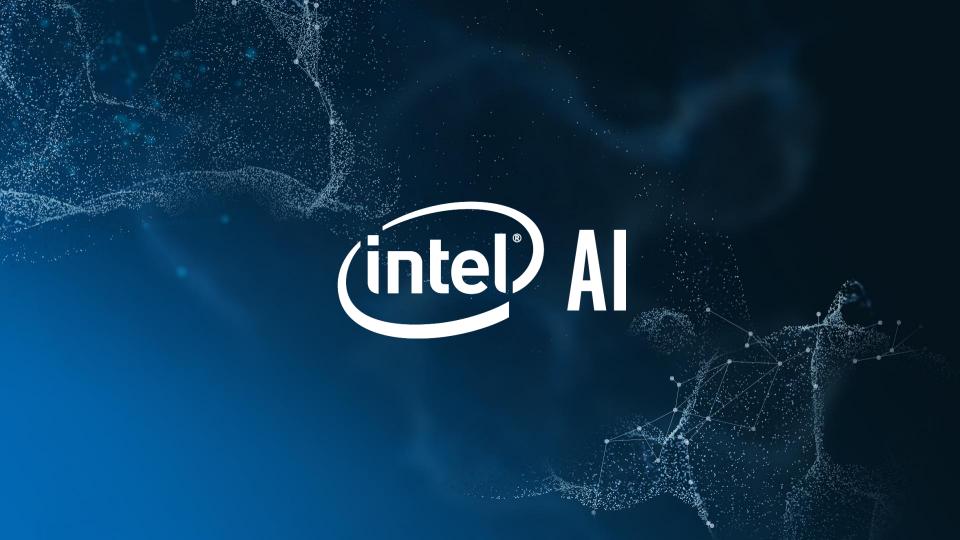


Phil Hummel Sr. Principal Engineer

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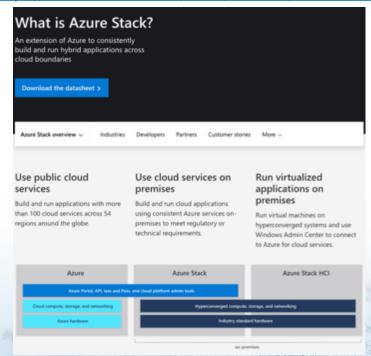


HYBRID DEPLOYMENTS

https://aws.amazon.com/outposts/



https://azure.microsoft.com/en-us/overview/azure-stack/



TO LEARN MORE

- Contact our partners or Intel at DeployAl@Intel.com
- Visit https://www.intel.ai/deploy-on-intel-architecture/





AI IN THE ENTERPRISE: THE INTEL® AI BUILDERS PARTNER SHOWCASE

- Intel® AI Builders Program: Benefits to partners and enterprise customers
- Why Intel® AI: Hardware, Software, Ecosystem
- Partner Showcase: Finance/ retail/ healthcare/ cross-industry AI solutions
- How to Deploy on Intel® Architecture: Optimized CSPs and OEM channels
- Match-Making: Continues until 7:30pm
- Happy Hour: Starts now!



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No product or component can be absolutely secure.

Tests document performance of components on a particular test, in specific systems. Differences in hardware, software, or configuration will affect actual performance. For more complete information about performance and benchmark results, visit https://www.intel.com/benchmarks.

Software and workloads used in performance tests may have been optimized for performance only on Intel microprocessors. Performance tests, such as SYSmark and MobileMark, are measured using specific computer systems, components, software, operations and functions. Any change to any of those factors may cause the results to vary. You should consult other information and performance tests to assist you in fully evaluating your contemplated purchases, including the performance of that product when combined with other products. For more complete information visit https://www.intel.com/benchmarks.

Intel® AVX)* provides higher throughput to certain processor operations. Due to varying processor power characteristics, utilizing AVX instructions may cause a) some parts to operate at less than the rated frequency and b) some parts with Intel® Turbo Boost Technology 2.0 to not achieve any or maximum turbo frequencies. Performance varies depending on hardware, software, and system configuration and you can learn more at http://www.intel.com/go/turbo.

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